

PCN Number:	20151028000	PCN Date:	2/4/2016
Title:	Datasheet for TPS65218, TPS65218B101		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
		<input type="checkbox"/>	Wafer Bump Site
		<input type="checkbox"/>	Wafer Bump Material
		<input type="checkbox"/>	Wafer Bump Process
		<input type="checkbox"/>	Wafer Fab Site
		<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification etc.

The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



TPS65218

SLDS206B –NOVEMBER 2014 –REVISED FEBRUARY 2016

Changes from Revision A (September 2015) to Revision B

Page

- Updated description for PGOOD to clarify that the output can be configured as open drain 3
- Added V_{IN_LS3} conditions to I_{LIMIT} for Load Switch 3 and updated the values 13

Changes from Original (November 2014) to Revision A

Page

- Added part number TPS65218B101 1
- Increased VIN range for low voltage load switch with 350-mA current limit 1
- Moved T_{stg} to the *Absolute Maximum Ratings* table and updated *Handling Ratings* table to an *ESD Ratings* table 5
- Added device part number to *Thermal Information* table 6
- Added test conditions and values for V_{UVLO} hysteresis 7
- Changed test conditions for input voltage ranges from " $V_{IN_BIAS} > 2.7 V$ " to " $V_{IN_BIAS} > V_{UVLO}$ " 7
- Updated values for DCDC1-4 V_{OUT} falling and rising 8
- Added more test conditions and values for V_{IN_DCDC4} 9
- Updated V_{DCDC4} test conditions and values and added output voltage ripple specification for PFM mode 9
- Updated test conditions and added new values for V_{DCDC5} , V_{DCDC6} DC accuracy 10
- Updated V_{IN_LS1} max value and added additional test condition for $R_{DS(ON)}$ 12
- Added note for t_{HIGH} 15
- Updated *f²C Bus Operation* and added a note 39
- Added note to *Application and Implementation* 72
- Added *Community Resources* 77

The datasheet number will be changing.

Device Family	Change From:	Change To:
TPS65218, TPS65218B101	SLDS206	SLDS206B

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/TPS65218>

Reason for Change:			
To more accurately reflect device characteristics.			
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):			
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.			
Changes to product identification resulting from this PCN:			
None.			
Product Affected:			
TPS65218B101PHPR	TPS65218B101PHPT	TPS65218B1PHPR	TPS65218B1PHPT
TPS65218B1RSLR	TPS65218B1RSLT		

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
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Asia Pacific	PCNAsiaContact@list.ti.com
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