PCN I	Number:	20190327001.0				PC	N Date:	Mar 28 2019
Title:	Notification o	f the addition of a Topside Inductor Coating on select devices						devices
Custo	omer Contact:	PCN M	<u>anager</u>	Dept:	Quality Services			
Chan	ge Type:							
	Assembly Site		Assembly Process			Assembly Materials		
Design			Datasheet				Mechanic	cal Specification
Test Site		$\boxtimes$	Packing/Shipping/Labeling				Test Prod	cess
☐ Wafer Bump Site			Wafer Bump Material				Wafer Bu	imp Process
V	Vafer Fab Site		Wafer Fab Materials				Wafer Fa	b Process
			Part nu	mber ch	ange			

## **PCN Details**

## **Description of Change:**

This letter is to inform customers an improvement to the assembly process of the devices in the product affected section below. These devices will now be subject to a topside inductor coating step which will improve the marking legibility. See below.

What No Coating Topside Inductor Coating

Example Picture #1

Example Picture #2

Example Picture #2

APQC G4

TOPSIDE INDUCTOR Coating

TOPSIDE IND

## **Reason for Change:**

To improve AVI performance

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):								
None								
Changes to product identification resulting from this PCN:								
N/a								
Product Affected:								
TPSM84424MOLR	TPSM846C23MOLR	TPSM846C24MOLR	TPSM84824MOLR					
TPSM84624MOLR								

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
WW PCN Team	PCN www admin_team@list.ti.com