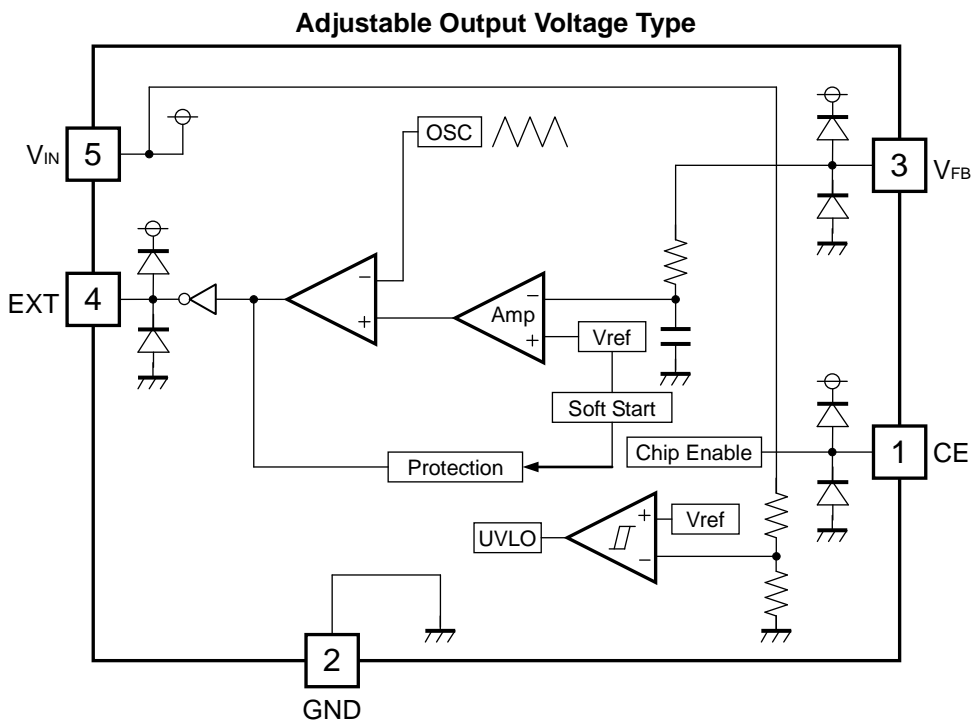
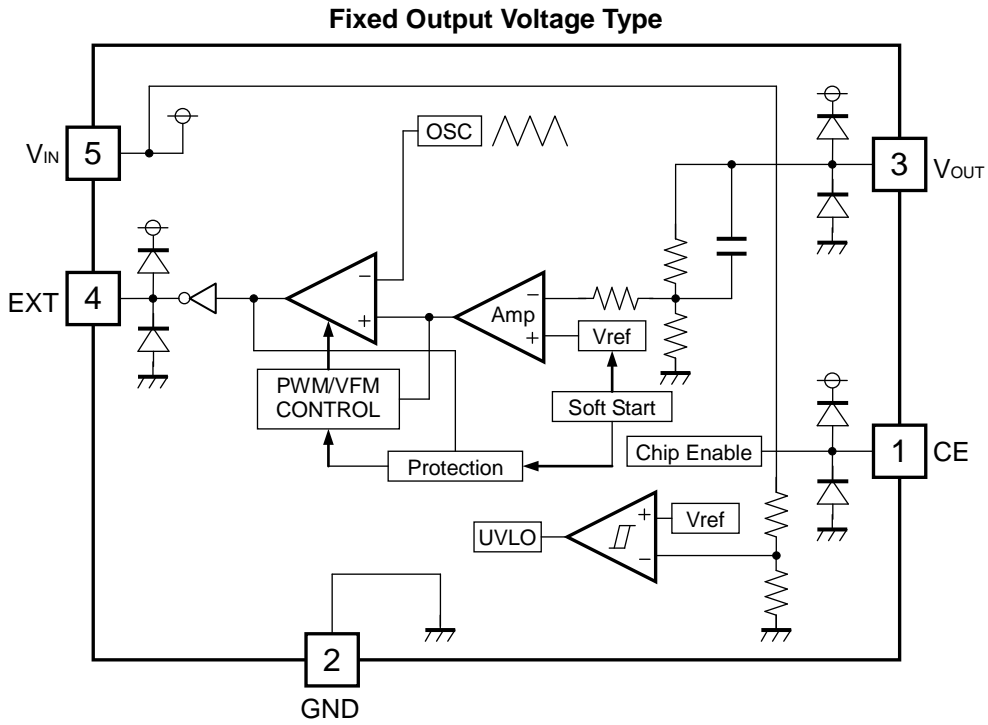


BLOCK DIAGRAM



SELECTION GUIDE

The output voltage, the oscillator frequency, the modulation method and the output voltage adjustment for the ICs can be selected at the user's request.

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R1224Nxx2*-TR-FE	SOT-23-5	3,000 pcs	Yes	Yes

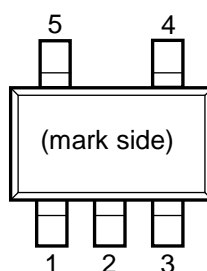
xx: The output voltage can be designated in the range from 1.2V(12) to 6.0V(60) in 0.1V steps.
(For externally adjustable output voltage type, feedback voltage of 1.0V(10).)

* : The oscillator frequency, the modulation method and the output voltage adjustment are options as follows.

Code	Oscillator frequency	PWM/VFM alternative circuit	Output voltage adjustment
E	300kHz	Yes	No
F	500kHz	Yes	No
G	300kHz	No	Yes
H	500kHz	No	Yes
L	180kHz	Yes	No
M	180kHz	No	Yes

PIN CONFIGURATION

• SOT-23-5



PIN DESCRIPTION

Pin No	Symbol	Pin Description
1	CE	Chip Enable Pin ("H" Active)
2	GND	Ground Pin
3	V_{OUT} (V_{FB})	Pin for Monitoring Output Voltage (Feedback Voltage)
4	EXT	External Transistor Drive Pin (CMOS Output)
5	V_{IN}	Power Supply Pin

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ABSOLUTE MAXIMUM RATINGS

GND=0V

Symbol	Item	Rating	Unit
V_{IN}	V_{IN} Supply Voltage	-0.3 to 20	V
V_{EXT}	EXT Pin Output Voltage	-0.3 to $V_{IN}+0.3$	V
V_{CE}	CE Pin Input Voltage	-0.3 to $V_{IN}+0.3$	V
V_{OUT}/V_{FB}	V_{OUT}/V_{FB} Pin Input Voltage	-0.3 to $V_{IN}+0.3$	V
I_{EXT}	EXT Pin Inductor Drive Output Current	± 50	mA
P_D	Power Dissipation (SOT-23-5)*	420	mW
T_{opt}	Operating Temperature Range	-40 to 85	°C
T_{stg}	Storage Temperature Range	-55 to 125	°C

*) For Power Dissipation, please refer to PACKAGE INFORMATION.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

ELECTRICAL CHARACTERISTICS

• R1224Nxx2x (x=E/F/G/H/L/M) except R1224N102x

T_{opt}=25°C

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V _{IN}	Operating Input Voltage		2.3		18.5	V
V _{OUT}	Step-down Output Voltage	V _{IN} =V _{CE} =V _{SET} +1.5V, I _{OUT} =-100mA When V _{SET} ≤1.5V, V _{IN} =V _{CE} =3.0V	V _{SET} ×0.98	V _{SET}	V _{SET} ×1.02	V
$\frac{\Delta V_{OUT}}{\Delta T_{opt}}$	Step-down Output Voltage Temperature Coefficient	-40°C≤T _{opt} ≤85°C		±100		ppm/°C
f _{osc}	Oscillator Frequency	V _{IN} =V _{CE} =V _{SET} +1.5V, I _{OUT} =-100mA L/M Version E/G Version F/H Version	144 240 400	180 300 500	216 360 600	kHz
$\frac{\Delta f_{osc}}{\Delta T_{opt}}$	Oscillator Frequency Temperature Coefficient	-40°C≤T _{opt} ≤85°C		±0.2		%/°C
I _{DD1}	Supply Current 1	V _{IN} =V _{CE} =V _{OUT} =18.5V E/F/L/M Version G version H version		20 30 40	50 60 80	μA
I _{standby}	Standby Current	V _{IN} =18.5V, V _{CE} =0V, V _{OUT} =0V		0	0.5	μA
I _{EXTH}	EXT "H" Output Current	V _{IN} =8V, V _{EXT} =7.9V, V _{OUT} =8V, V _{CE} =8V		-17	-10	mA
I _{EXTL}	EXT "L" Output Current	V _{IN} =8V, V _{EXT} =0.1V, V _{OUT} =0V, V _{CE} =8V	20	30		mA
I _{CEH}	CE "H" Input Current	V _{IN} =V _{CE} =V _{OUT} =18.5V		0	0.5	μA
I _{CEL}	CE "L" Input Current	V _{IN} =V _{OUT} =18.5V, V _{CE} =0V	-0.5	0		μA
V _{CEH}	CE "H" Input Voltage	V _{IN} =8V, I _{OUT} =-10mA	1.5			V
V _{CEL}	CE "L" Input Voltage	V _{IN} =8V, I _{OUT} =-10mA			0.3	V
Maxduty	Oscillator Maximum Duty Cycle		100			%
VFMdty	VFM Duty Cycle	E/F/L Version		35		%
V _{UVLO1}	UVLO Voltage	V _{IN} =V _{CE} =2.5V to 1.5V, V _{OUT} =0V	1.8	2.0	2.2	V
V _{UVLO2}	UVLO Release Voltage	V _{IN} =V _{CE} =1.5V to 2.5V, V _{OUT} =0V		V _{UVLO1} +0.1	2.3	V
t _{start}	Delay Time by Soft-Start function	V _{IN} =V _{SET} +1.5V, I _{OUT} =-10mA V _{CE} =0V→V _{SET} +1.5V	5	10	20	ms
t _{prot}	Delay Time for protection circuit	V _{IN} =V _{CE} =V _{SET} +1.5V V _{OUT} =V _{SET} +1.5V→0V	5	15	30	ms

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

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• R1224N102x (x=G/H/M)

T_{opt}=25°C

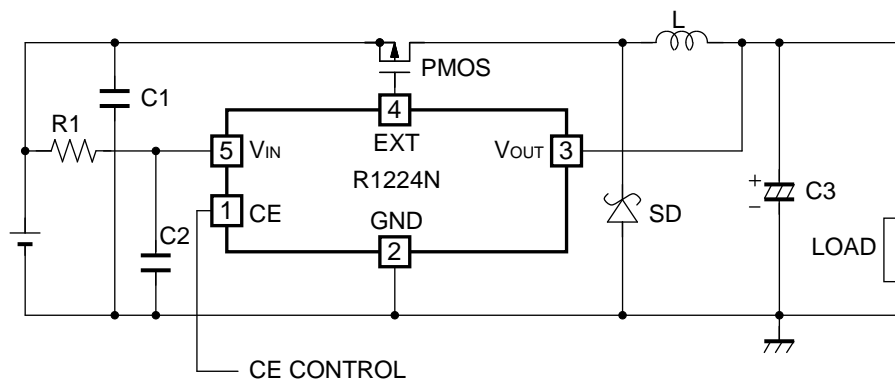
Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V _{IN}	Operating Input Voltage		2.3		18.5	V
V _{FB}	Feedback Voltage	V _{IN} =V _{CE} =3.0V, I _{OUT} =-100mA	0.98	1.00	1.02	V
$\frac{\Delta V_{FB}}{\Delta T_{opt}}$	Feedback Voltage Temperature Coefficient	-40°C ≤ T _{opt} ≤ 85°C		±100		ppm/°C
f _{osc}	Oscillator Frequency	V _{IN} =V _{CE} =2.5V, I _{OUT} =-100mA M Version G Version H Version	144 240 400	180 300 500	216 360 600	kHz
$\frac{\Delta f_{osc}}{\Delta T_{opt}}$	Oscillator Frequency Temperature Coefficient	-40°C ≤ T _{opt} ≤ 85°C		±0.2		%/°C
I _{DD1}	Supply Current 1	V _{IN} =V _{CE} =V _{FB} =18.5V M Version G Version H Version		20 30 40	50 60 80	μA
I _{standby}	Standby Current	V _{IN} =18.5V, V _{CE} =0V, V _{FB} =0V		0	0.5	μA
I _{EXTH}	EXT "H" Output Current	V _{IN} =8V, V _{EXT} =7.9V, V _{FB} =8V, V _{CE} =8V		-17	-10	mA
I _{EXTL}	EXT "L" Output Current	V _{IN} =8V, V _{EXT} =0.1V, V _{FB} =0V, V _{CE} =8V	20	30		mA
I _{CEH}	CE "H" Input Current	V _{IN} =V _{CE} =V _{FB} =18.5V		0	0.5	μA
I _{CEL}	CE "L" Input Current	V _{IN} =V _{FB} =18.5V, V _{CE} =0V	-0.5	0		μA
V _{CEH}	CE "H" Input Voltage	V _{IN} =8V, I _{OUT} =-10mA	1.5			V
V _{CEL}	CE "L" Input Voltage	V _{IN} =8V, I _{OUT} =-10mA			0.3	V
Maxduty	Oscillator Maximum Duty Cycle		100			%
V _{UVLO1}	UVLO Voltage	V _{IN} =V _{CE} =2.5V to 1.5V, V _{FB} =0V	1.8	2.0	2.2	V
V _{UVLO2}	UVLO Release Voltage	V _{IN} =V _{CE} =1.5V to 2.5V, V _{FB} =0V		V _{UVLO1} +0.1	2.3	V
t _{start}	Delay Time by Soft-Start function	V _{IN} =2.5V, I _{OUT} =-10mA V _{CE} =0V → 2.5V	5	10	20	ms
t _{prot}	Delay Time for protection circuit	V _{IN} =V _{CE} =2.5V V _{FB} =2.5V → 0V	5	15	30	ms

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

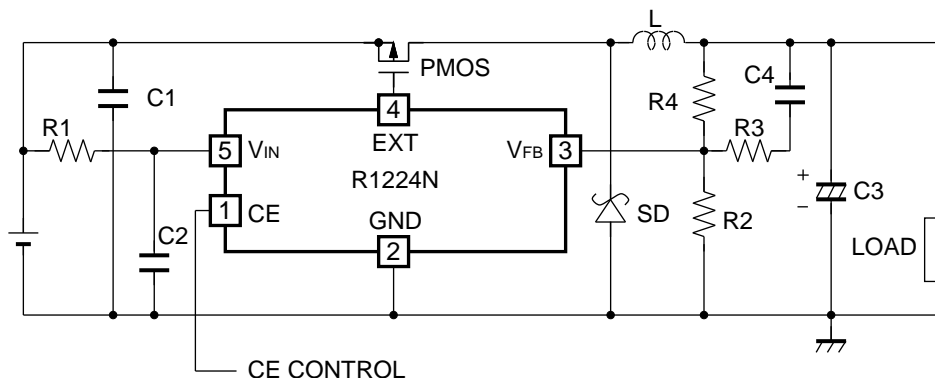
TYPICAL APPLICATION AND APPLICATION HINTS

(1) Fixed Output Voltage Type (R1224Nxx2E/F/G/H/L/M except xx=10)



PMOS: uPA1914 (Renesas) L : CR105NP-270MC (Sumida, 27 μ H)
 SD1 : CMS06 (TOSHIBA) C3 : 47 μ F (Tantalum Type)
 C1 : 10 μ F (Ceramic Type) C2 : 0.1 μ F (Ceramic Type)
 R1 : 10 Ω

(2) Adjustable Output Type (R1224N102G/H/M) Example: Output Voltage=3.2V



PMOS: uPA1914 (Renesas) L : CR105NP-270MC (Sumida, 27 μ H)
 SD1 : CMS06 (TOSHIBA) C3 : 47 μ F (Tantalum Type)
 C1 : 10 μ F (Ceramic Type) C2 : 0.1 μ F (Ceramic Type) C4: 1000pF (Ceramic Type)
 R1 : 10 Ω , R2=22k Ω , R3=2.7k Ω , R4=33k Ω

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When you use these ICs, consider the following issues;

·As shown in the block diagram, a parasitic diode is formed in each terminal, each of these diodes is not formed for load current, therefore do not use it in such a way. When you control the CE pin by another power supply, do not make its "H" level more than the voltage level of V_{IN} pin.

·Set external components as close as possible to the IC and minimize the connection between the components and the IC. In particular, a capacitor should be connected to V_{IN} pin with the minimum connection. Make sufficient ground and reinforce supplying. A large switching current could flow through the connection of power supply, an inductor and the connection of V_{IN} . If the impedance of the connection of power supply is high, the voltage level of power supply of the IC fluctuates with the switching current. This may cause unstable operation of the IC.

·Protection circuit may work if the maximum duty cycle continue for the time defined in the electrical characteristics. Once after stopping the output voltage, output will restart with soft-start operation. If the difference between input voltage and output voltage is small, the protection circuit may work.

·Use capacitors with a capacity of 22 μ F or more for V_{OUT} pin, and with good high frequency characteristics such as tantalum capacitors. We recommend you to use output capacitors with an allowable voltage at least twice as much as setting output voltage. This is because there may be a case where a spike-shaped high voltage is generated by an inductor when an external transistor is on and off.

·Choose an inductor that has sufficiently small D.C. resistance and large allowable current and is hard to reach magnetic saturation. And if the value of inductance of an inductor is extremely small, the I_{LX} may exceed the absolute maximum rating at the maximum loading.
Use an inductor with appropriate inductance.

·Use a diode of a Schottky type with high switching speed, and also pay attention to its current capacity.

·Do not use this IC under the condition with V_{IN} voltage at equal or less than minimum operating voltage.

·When the threshold level of an external power MOSFET is rather low and the drive-ability of voltage supplier is small, if the output pin is short circuit, input voltage may be equal or less than UVLO detector threshold. In this case, the device is reset with UVLO function that is different from the reset-protection function caused by maximum duty cycle.

·With the PWM/VFM alternative circuit, when the on duty cycle of switching is 35% or less, the R1224N alters from PWM mode to VFM mode (Pulse skip mode). The purpose of this circuit is raising the efficiency with a light load by skipping the frequency and suppressing the consumption current. However, the ratio of output voltage against input voltage is 35% or less, (ex. $V_{IN}>8.6V$ and $V_{OUT}=3.0V$) even if the large current may be loaded, the IC keeps its VFM mode. As a result, frequency might be decreased, and oscillation waveform might be unstable. These phenomena are the typical characteristics of the IC with PWM/VFM alternative circuit.

·If the input voltage is equal or more than 6V, R1 and C2 in the typical application are necessary as a V_{IN} filter to prevent unstable operation.

★The performance of power source circuits using these ICs extremely depends upon the peripheral circuits. Pay attention in the selection of the peripheral circuits. In particular, design the peripheral circuits in a way that the values such as voltage, current, and power of each component, PCB patterns and the IC do not exceed their respected rated values.

How to Adjust Output Voltage and about Phase Compensation

As for Adjustable Output type, feedback pin (V_{FB}) voltage is controlled to maintain 1.0V.
Output Voltage, V_{OUT} is as following equation:

$$V_{OUT}: R2+R4=V_{FB}: R2$$

$$V_{OUT}=V_{FB}\times(R2+R4)/R2$$

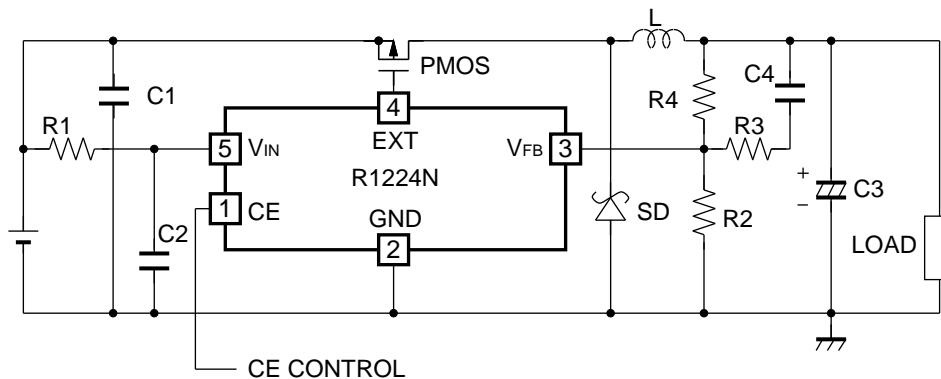
Thus, with changing the value of R2 and R4, output voltage can be set in the specified range.
In the DC/DC converter, with the load current and external components such as L and C, phase might be behind 180 degree. In this case, the phase margin of the system will be less and stability will be worse. To prevent this, phase margin should be secured with proceeding the phase. A pole is formed with external components L and C3.

$$F_{pole} \sim 1/2\pi\sqrt{L \times C3}$$

A zero (signal back to zero) is formed with R4 and C4.

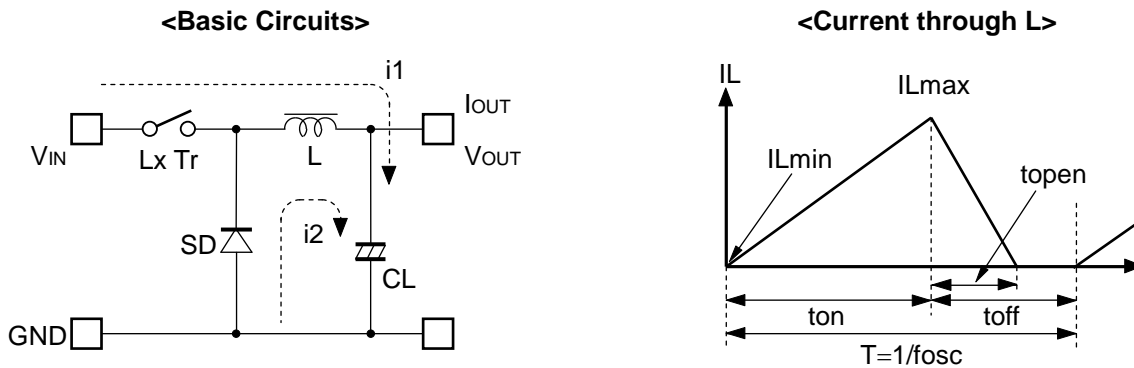
$$\cong F_{zero} \sim 1/(2\pi \times R4 \times C4)$$

For example, if $L=27\mu\text{H}$, $C3=47\mu\text{F}$, the cut off frequency of the pole is approximately 4.5kHz.
To make the cut off frequency of the pole as much as 4.5kHz, set $R4=33\text{k}\Omega$ and $C4=1000\text{pF}$.
If V_{OUT} is set at 2.5V, $R2=22\text{k}\Omega$ is appropriate.
 $R3$ prevents feedback of the noise to V_{FB} pin, about $2.7\text{k}\Omega$ is appropriate value.



OPERATION of step-down DC/DC converter and Output Current

The step-down DC/DC converter charges energy in the inductor when Lx transistor is ON, and discharges the energy from the inductor when Lx transistor is OFF and controls with less energy loss, so that a lower output voltage than the input voltage is obtained. The operation will be explained with reference to the following diagrams:



- Step 1: Lx Tr. turns on and current $I_L (=i_1)$ flows, and energy is charged into CL. At this moment, I_L increases from I_{Lmin} . ($=0$) to reach I_{Lmax} . in proportion to the on-time period (t_{on}) of Lx Tr.
- Step 2: When Lx Tr. turns off, Schottky diode (SD) turns on in order that L maintains I_L at I_{Lmax} , and current $I_L (=i_2)$ flows.
- Step 3: I_L decreases gradually and reaches I_{Lmin} . after a time period of t_{open} , and SD turns off, provided that in the continuous mode, next cycle starts before I_L becomes to 0 because t_{off} time is not enough. In this case, I_L value is from this I_{Lmin} (>0).

In the case of PWM control system, the output voltage is maintained by controlling the on-time period (t_{on}), with the oscillator frequency (f_{osc}) being maintained constant.

Discontinuous Conduction Mode and Continuous Conduction Mode

The maximum value (I_{Lmax}) and the minimum value (I_{Lmin}) current which flow through the inductor is the same as those when Lx Tr. is ON and when it is OFF.

The difference between I_{Lmax} and I_{Lmin} , which is represented by ΔI ;

$$\Delta I = I_{Lmax} - I_{Lmin} = V_{OUT} \times t_{open} / L = (V_{IN} - V_{OUT}) \times t_{on} / L \dots\dots\dots \text{Equation 1}$$

wherein, $T = 1/f_{osc} = t_{on} + t_{off}$
 duty (%) = $t_{on} / T \times 100 = t_{on} \times f_{osc} \times 100$
 $t_{open} \leq t_{off}$

In Equation 1, $V_{OUT} \times t_{open} / L$ and $(V_{IN} - V_{OUT}) \times t_{on} / L$ are respectively shown the change of the current at ON, and the change of the current at OFF.

When the output current (I_{OUT}) is relatively small, $t_{open} < t_{off}$ as illustrated in the above diagram. In this case, the energy is charged in the inductor during the time period of t_{on} and is discharged in its entirely during the time period of t_{off} , therefore I_{Lmin} becomes to zero ($I_{Lmin} = 0$). When I_{OUT} is gradually increased, eventually, t_{open} becomes to t_{off} ($t_{open} = t_{off}$), and when I_{OUT} is further increased, I_{Lmin} becomes larger than zero ($I_{Lmin} > 0$). The former mode is referred to as the discontinuous mode and the latter mode is referred to as continuous mode.

In the continuous mode, when Equation 1 is solved for t_{on} and assumed that the solution is t_{onc} ,

$$t_{onc} = T \times V_{OUT} / V_{IN} \dots \dots \dots \text{Equation 2}$$

When $t_{on} < t_{onc}$, the mode is the discontinuous mode, and when $t_{on} = t_{onc}$, the mode is the continuous mode.

OUTPUT CURRENT AND SELECTION OF EXTERNAL COMPONENTS

When Lx Tr. is ON:

(Wherein, Ripple Current P-P value is described as I_{RP} , ON resistance of Lx Tr. is described as R_p the direct current of the inductor is described as R_L .)

$$V_{IN} = V_{OUT} + (R_p + R_L) \times I_{OUT} + L \times I_{RP} / t_{on} \dots \dots \dots \text{Equation 3}$$

When Lx Tr. is OFF:

$$L \times I_{RP} / t_{off} = V_F + V_{OUT} + R_L \times I_{OUT} \dots \dots \dots \text{Equation 4}$$

Put Equation 4 to Equation 3 and solve for ON duty, $t_{on} / (t_{off} + t_{on}) = D_{ON}$,

$$D_{ON} = (V_{OUT} + V_F + R_L \times I_{OUT}) / (V_{IN} + V_F - R_p \times I_{OUT}) \dots \dots \dots \text{Equation 5}$$

Ripple Current is as follows;

$$I_{RP} = (V_{IN} - V_{OUT} - R_p \times I_{OUT} - R_L \times I_{OUT}) \times D_{ON} / f / L \dots \dots \dots \text{Equation 6}$$

Wherein, peak current that flows through L, Lx Tr., and SD is as follows;

$$I_{Lmax} = I_{OUT} + I_{RP} / 2 \dots \dots \dots \text{Equation 7}$$

Consider I_{Lmax} , condition of input and output and select external components.

★ The above explanation is directed to the calculation in an ideal case in continuous mode.

External Components

1. Inductor

Select an inductor that peak current does not exceed I_{Lmax} . If larger current than allowable current flows, magnetic saturation occurs and make transform efficiency worse.

When the load current is definite, the smaller value of L , the larger the ripple current.

Provided that the allowable current is large in that case and DC current is small, therefore, for large output current, efficiency is better than using an inductor with a large value of L and vice versa.

2. Diode

Use a diode with low V_F (Schottky type is recommended.) and high switching speed.

Reverse voltage rating should be more than V_{IN} and current rating should be equal or more than I_{Lmax} .

3. Capacitors

As for C_{IN} , use a capacitor with low ESR (Equivalent Series Resistance) and a capacity of at least $10\mu F$ for stable operation.

C_{OUT} can reduce ripple of Output Voltage, therefore $47\mu F$ or more value of tantalum type capacitor is recommended.

4. Lx Transistor

Pch Power MOSFET is required for this IC.

Its breakdown voltage between gate and source should be a few V higher than Input Voltage.

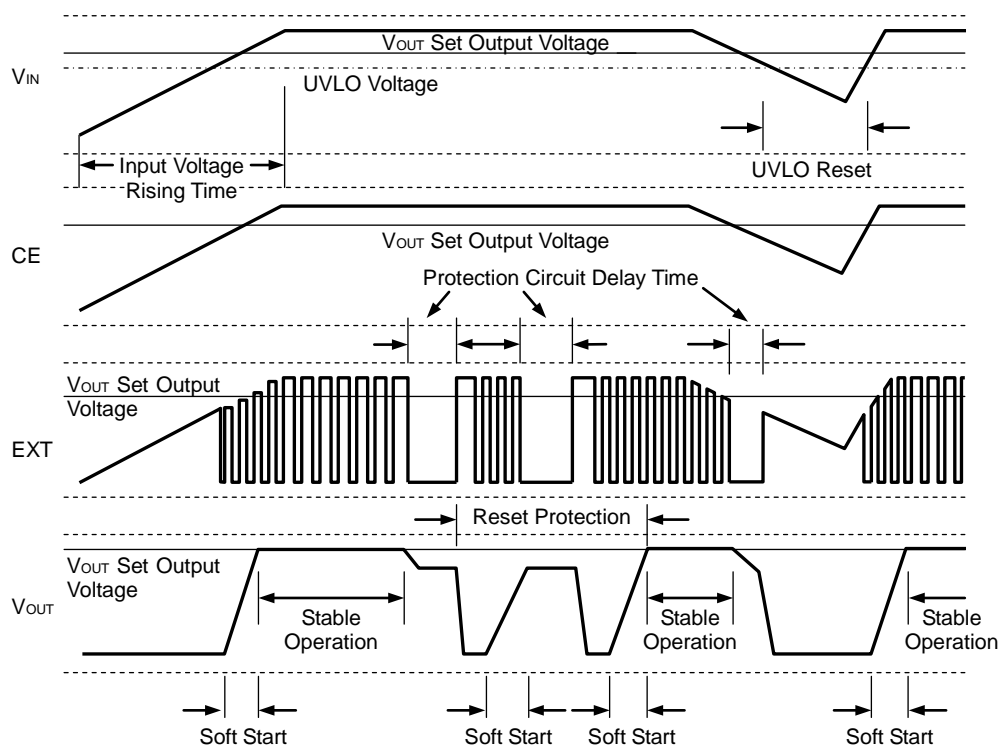
In the case of Input Voltage is low, to turn on MOSFET completely, to use a MOSFET with low threshold voltage is effective.

If a large load current is necessary for your application and important, choose a MOSFET with low ON resistance for good efficiency.

If a small load current is mainly necessary for your application, choose a MOSFET with low gate capacity for good efficiency.

Maximum continuous drain current of MOSFET should be larger than peak current, I_{Lmax} .

TIMING CHART



The timing chart shown above describes the changing process of input voltage rising, stable operating, operating with large current, stable operating, input voltage falling, input voltage recovering, and stable operating.

First, until when the input voltage (V_{IN}) reaches UVLO voltage, the circuit inside keeps the condition of pre-standby.

Second, after V_{IN} becomes beyond the UVLO threshold, soft-start operation starts, when the soft-start operation finishes, the operation becomes stable.

If too large current flows through the circuit because of short or other reasons, EXT signal ignores that during the delay time of protection circuit. (The current value depends on the circuit.)

After the delay time passes, reset protection works, or EXT signal will be "H", then output will turn off, then soft-start operation starts. After the soft-start operation, EXT signal will be "L", but if the large current is still flowing, after the delay time of protection circuit passes, reset protection circuit will work again, the operation will be continuously repeated unless the cause of large current flowing is not removed.

Once the cause of the large current flowing is removed, within the delay time, the operation will be back to the stable one.

If the timing for release the large current is in the protection process, the operation will be back to the normal one after the soft-start operation.

If the V_{IN} becomes lower than the set V_{OUT} , that situation is same as large current condition, so protection circuit may be ready to work, therefore, after the delay time of protection circuit, EXT will be "H".

Further, if the V_{IN} is lower than UVLO voltage, the circuit inside will be stopped by UVLO function.

After that, if V_{IN} rises, until when the V_{IN} reaches UVLO voltage, the circuit inside keeps the condition of pre-standby.

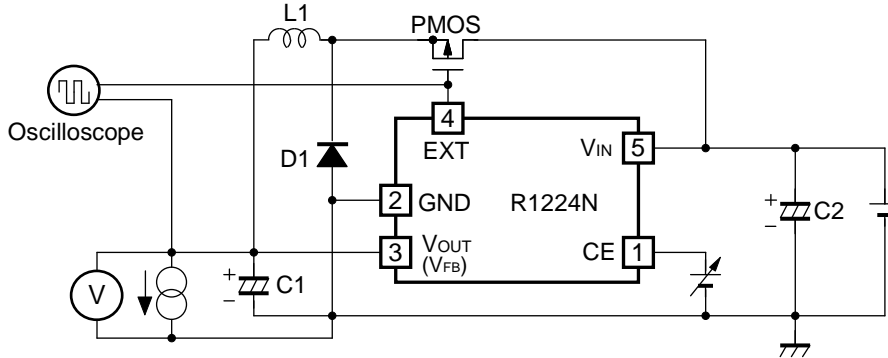
Then after V_{IN} becomes beyond the UVLO threshold, soft-start operation starts, when the soft-start operation finishes, the operation becomes stable.

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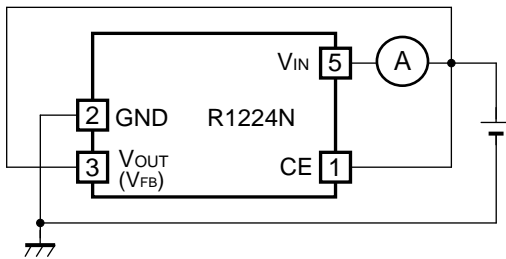
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TEST CIRCUITS

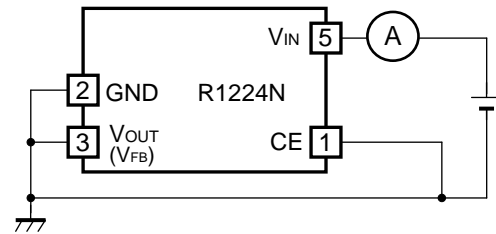
Output Voltage, Oscillator Frequency, CE "H" Input Voltage, CE "L" Input Voltage, Soft-start time



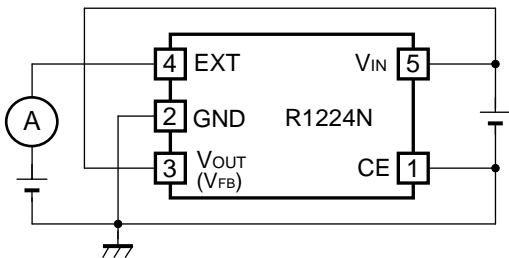
Supply Current 1



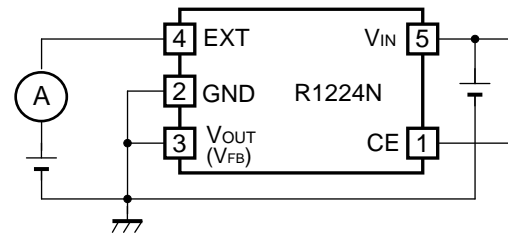
Standby Current



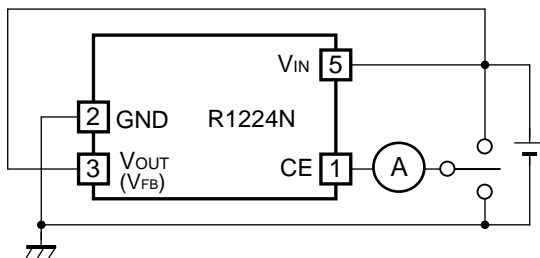
EXT "H" Output Current



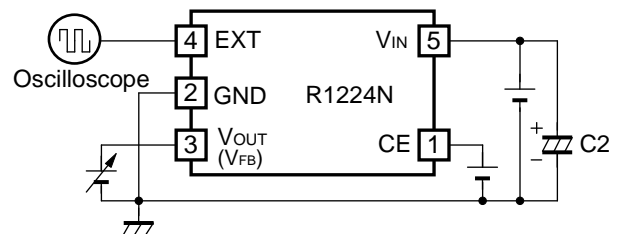
EXT "L" Output Current



CE "H" Input Current, CE "L" Input Current



Output Delay Time for Protection Circuit

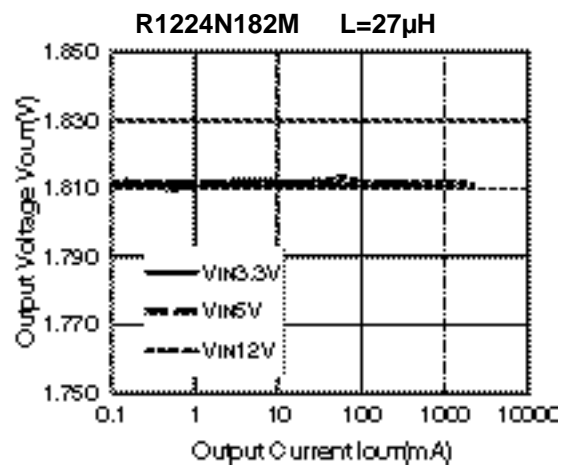
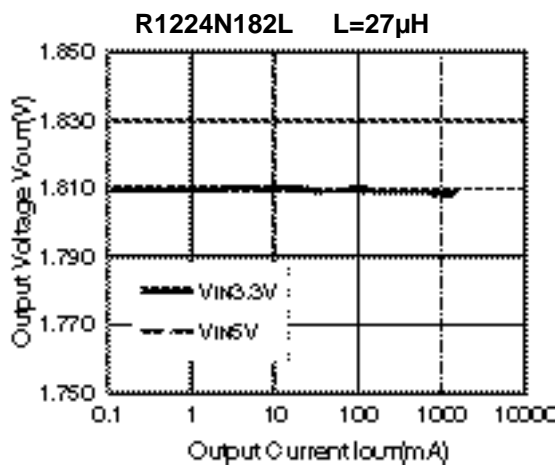
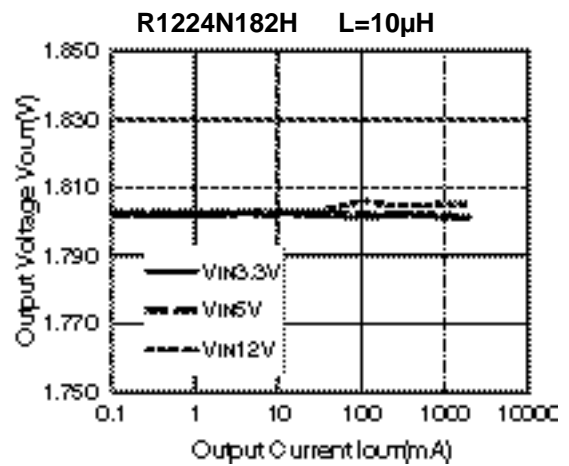
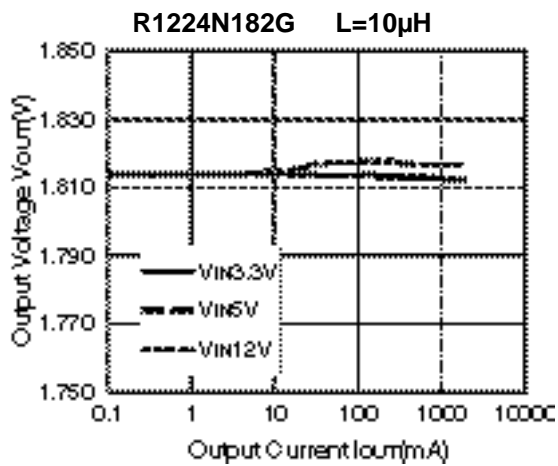
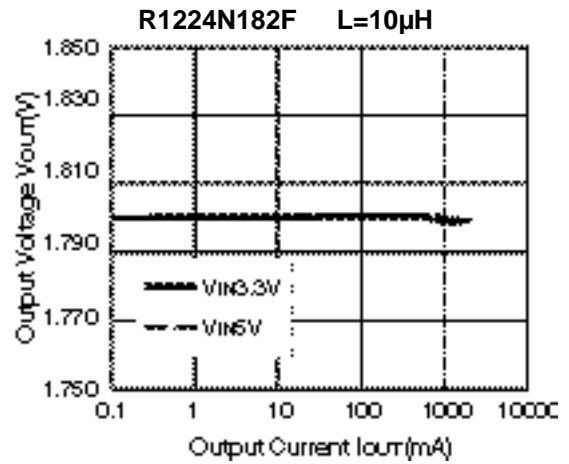
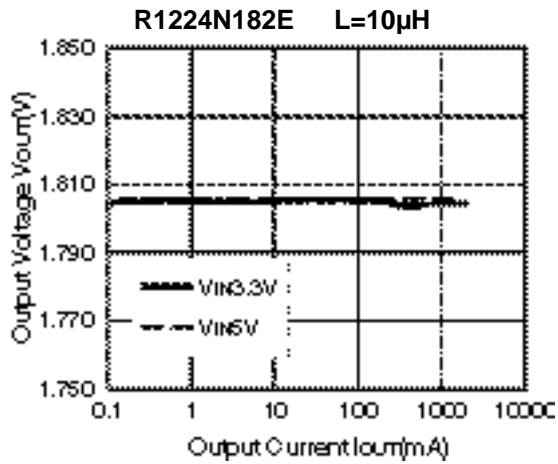


PMOS: HAT1044M (Hitachi)
SD1 : RB491D (Rohm)
C1 : 47 μ F (Tantalum Type)

L : CD104NP-270MC (Sumida, 27 μ H)
C2: 47 μ F (Tantalum Type)

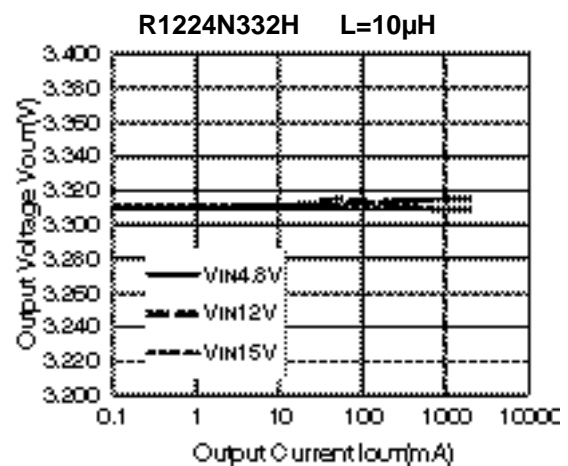
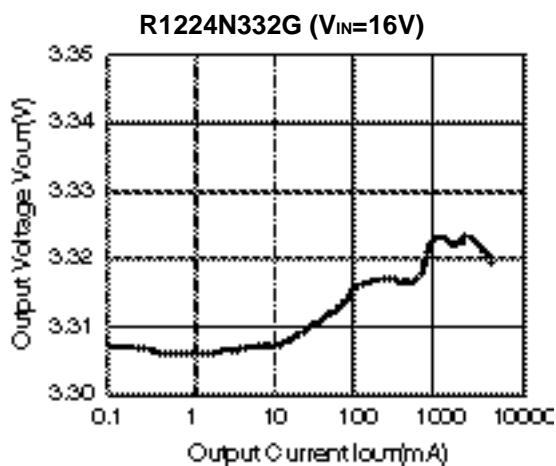
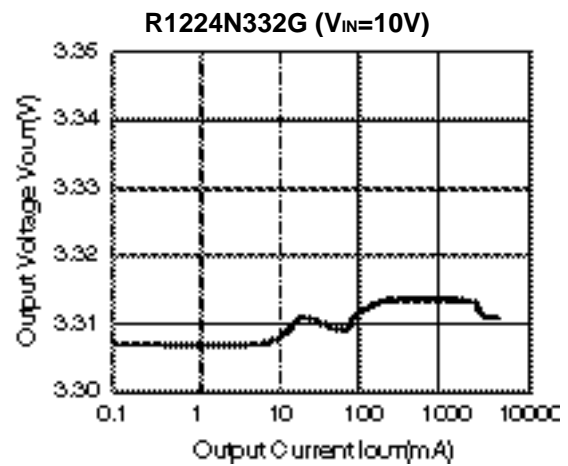
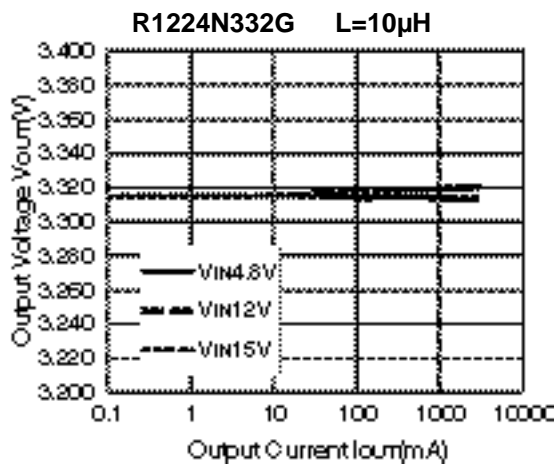
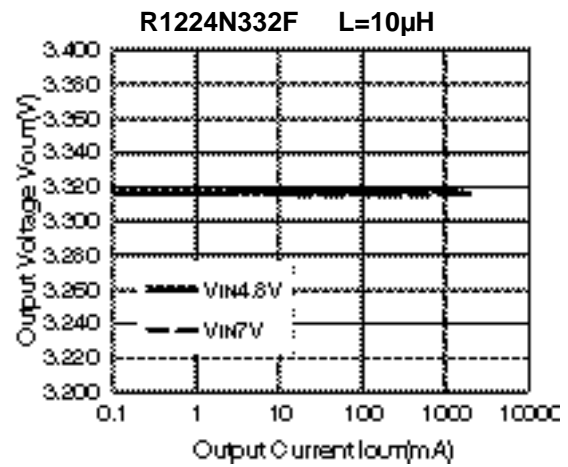
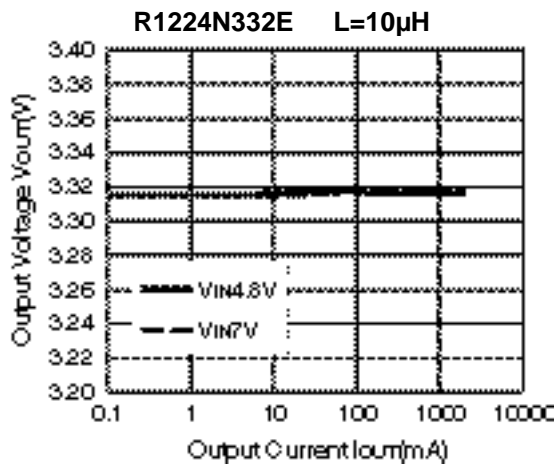
TYPICAL CHARACTERISTICS

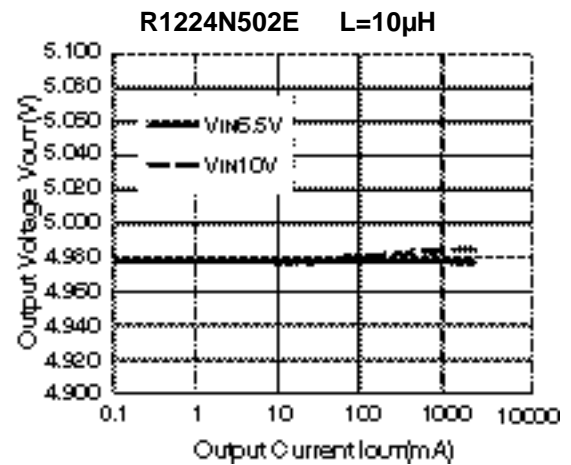
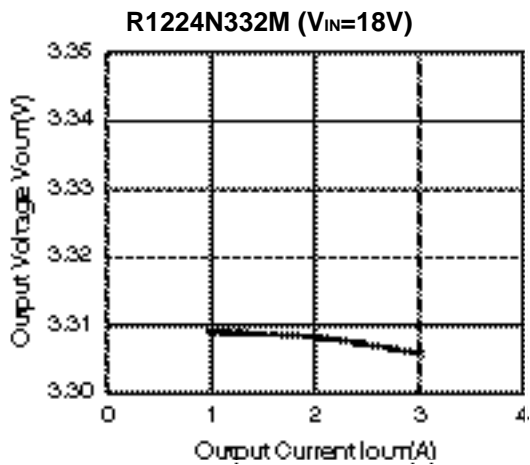
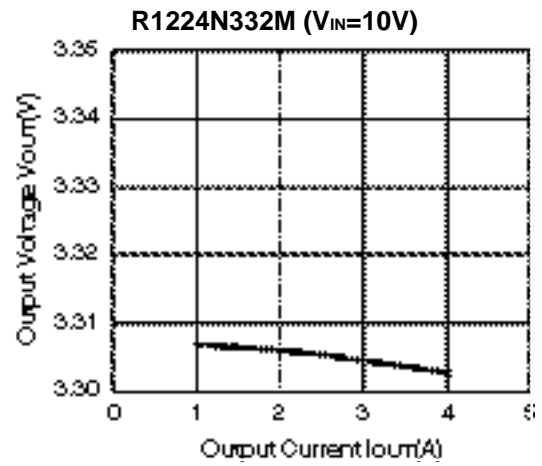
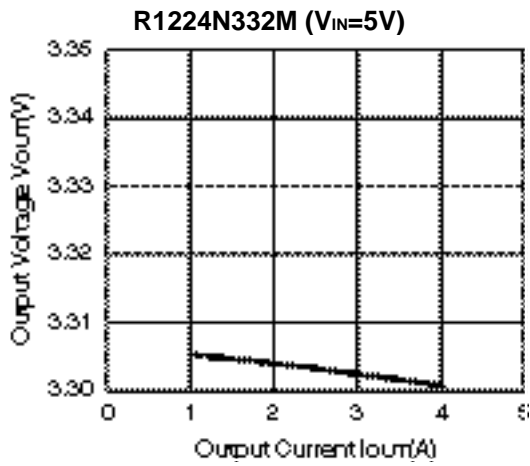
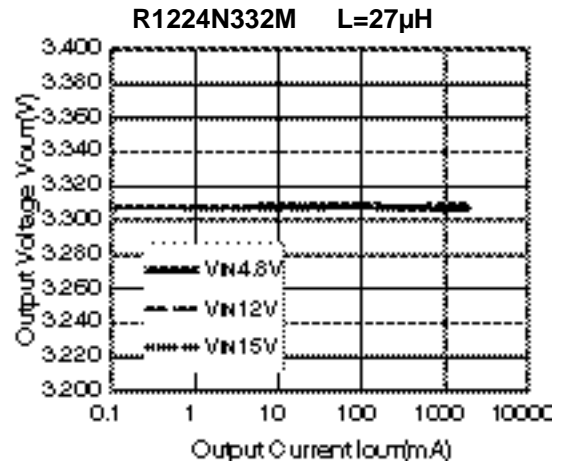
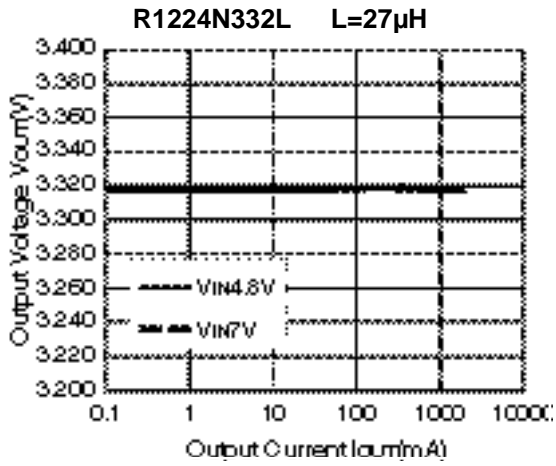
1) Output Voltage vs. Output Current (*Note)



R1224N

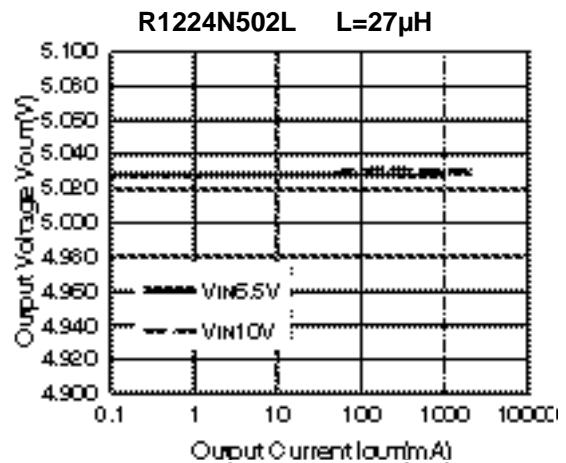
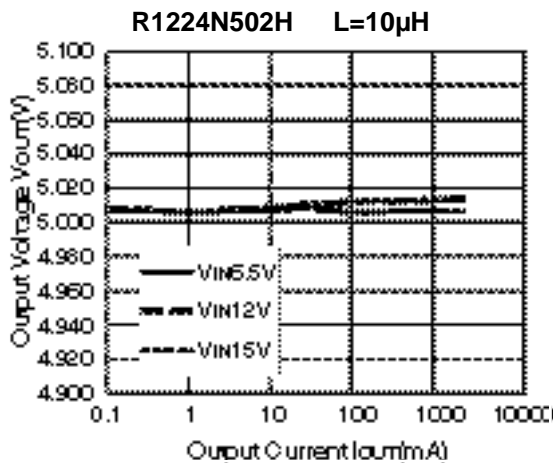
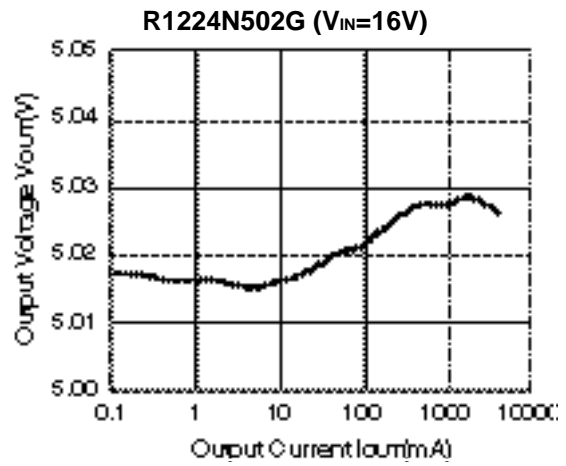
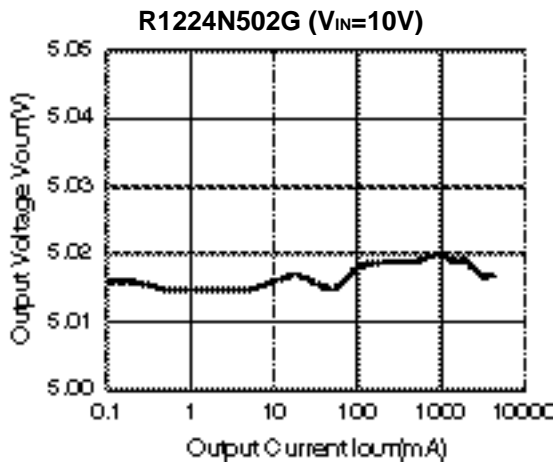
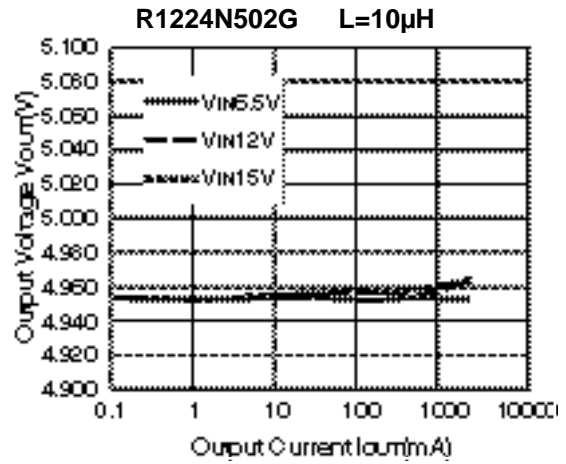
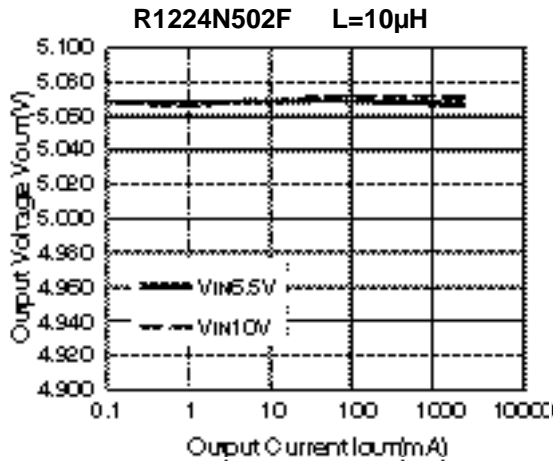
NO.EA-096-181004



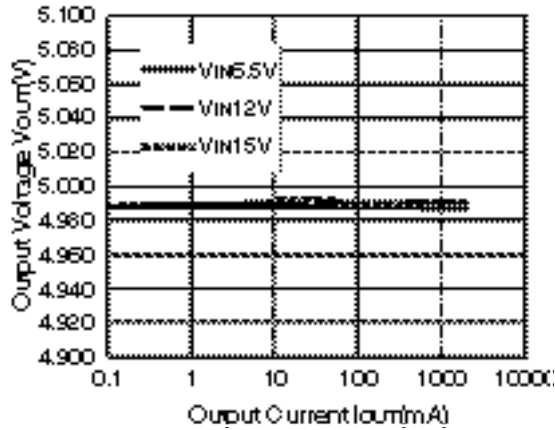


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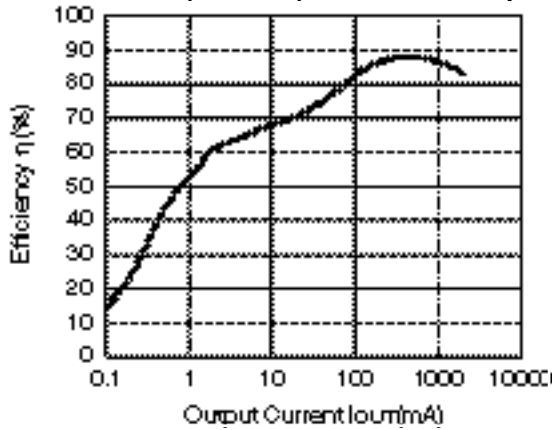
R1224N502M L=27 μ H



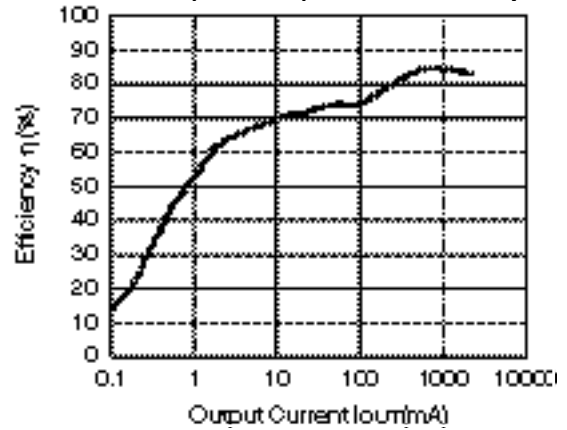
*Note: Typical characteristics 1) are obtained with using the following components;
 PMOS: IRF7406 (IR)
 L : CDRH127-100MC (Sumida: 10 μ H)
 SD : RB083L-20 (Rohm)
 C1 : 25SC47 (Sanyo/OS-con: 47 μ F/25V) \times 2
 C2 : 0.1 μ F (Ceramic Type)
 C3 : 10SA220 (Sanyo/OS-con: 220 μ F/10V)
 R1 : 10 Ω

2) Efficiency vs. Output Current (*Note)

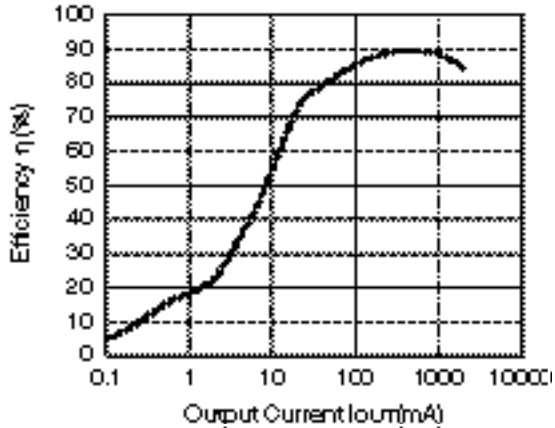
R1224N182F (VIN=3.3V) CDRH127-10 μ H



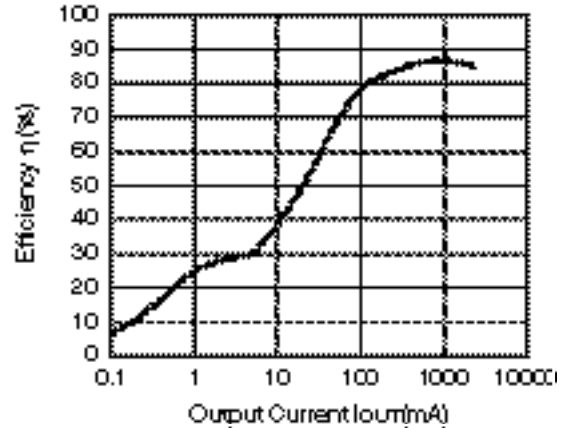
R1224N182F (VIN=5.0V) CDRH127-10 μ H



R1224N182G (VIN=3.3V) CDRH127-10 μ H



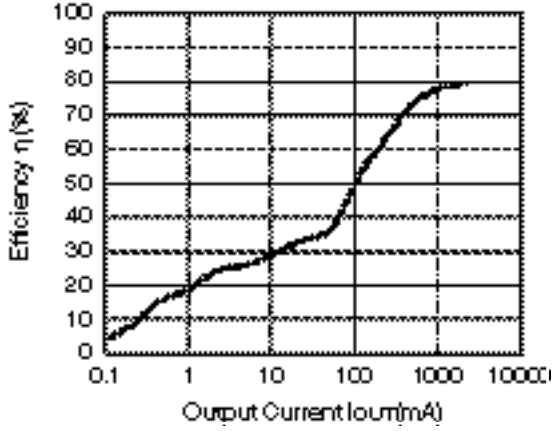
R1224N182G (VIN=5.0V) CDRH127-10 μ H



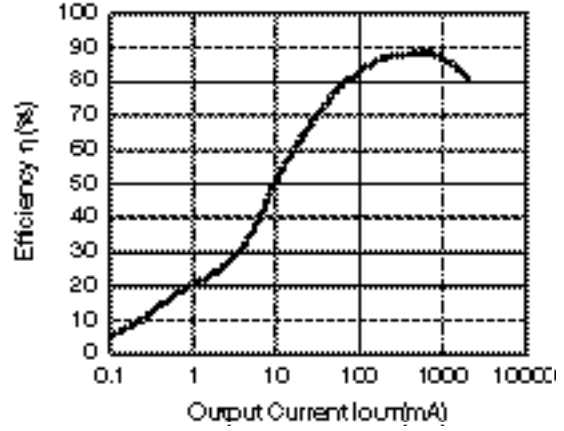
R1224N

NO.EA-096-181004

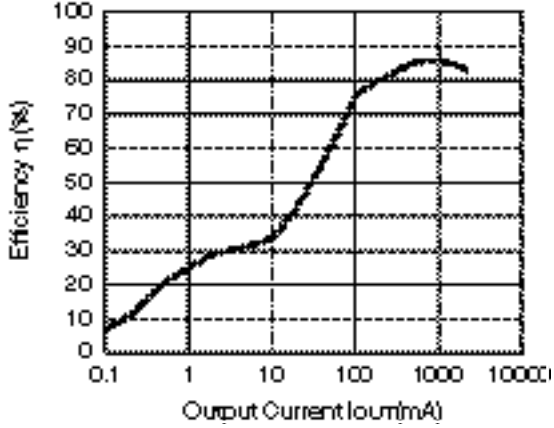
R1224N182G ($V_{IN}=12V$) CDRH127-10 μ H



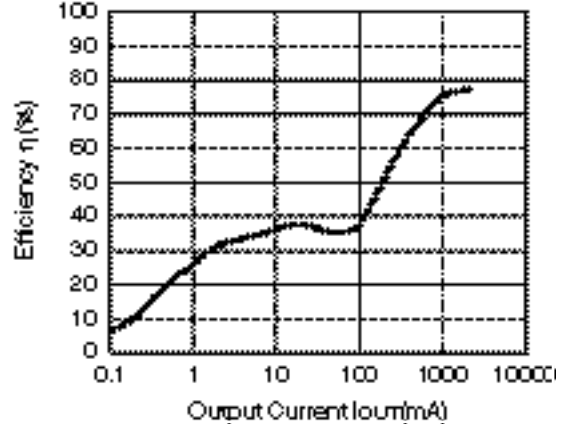
R1224N182H ($V_{IN}=3.3V$) CDRH127-10 μ H



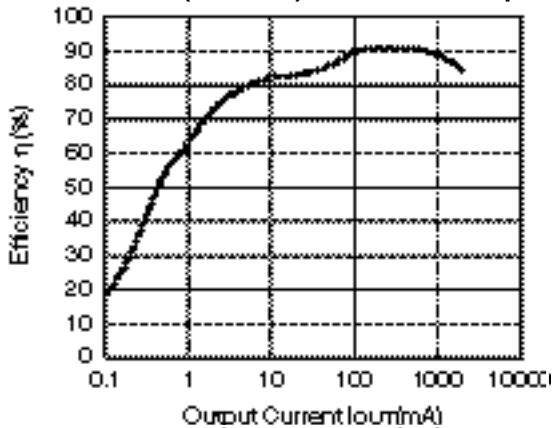
R1224N182H ($V_{IN}=5.0V$) CDRH127-10 μ H



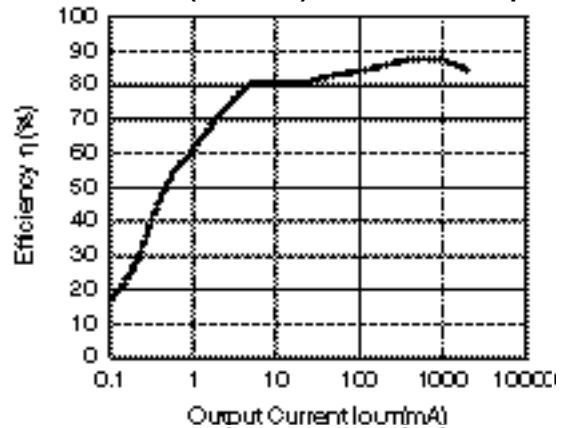
R1224N182H ($V_{IN}=12V$) CDRH127-10 μ H



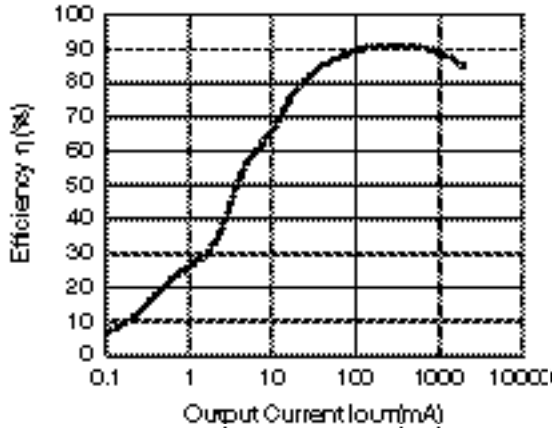
R1224N182L ($V_{IN}=3.3V$) CDRH127-27 μ H



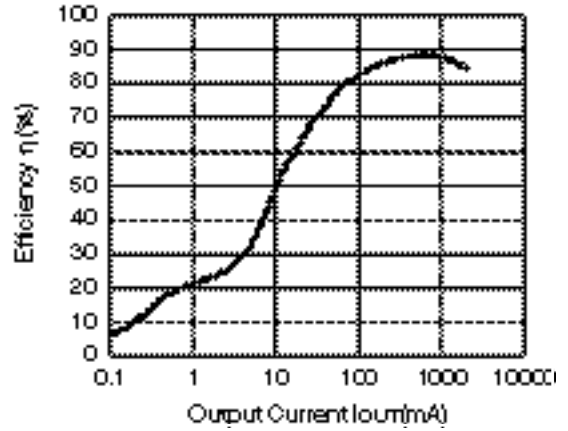
R1224N182L ($V_{IN}=5.0V$) CDRH127-27 μ H



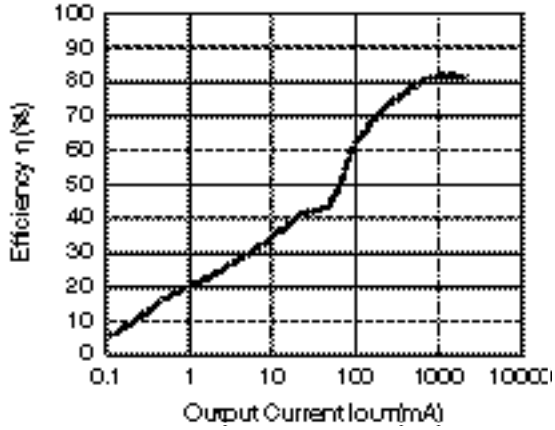
R1224N182M (V_{IN}=3.3V) CDRH127-27μH



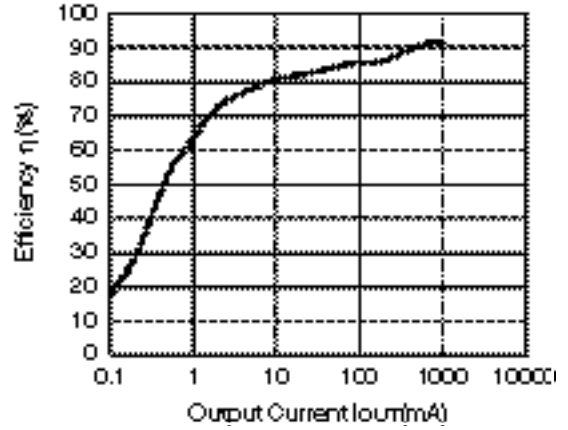
R1224N182M (V_{IN}=5.0V) CDRH127-27μH



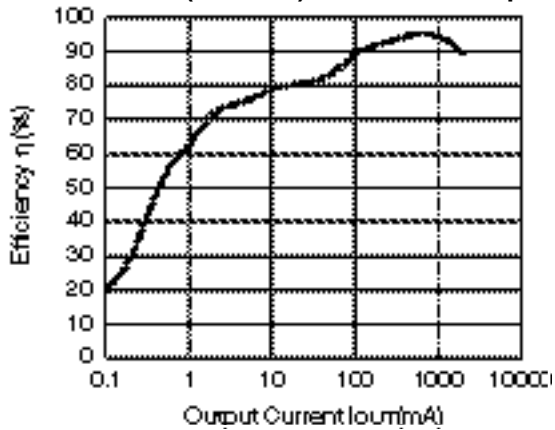
R1224N182M (V_{IN}=12V) CDRH127-27μH



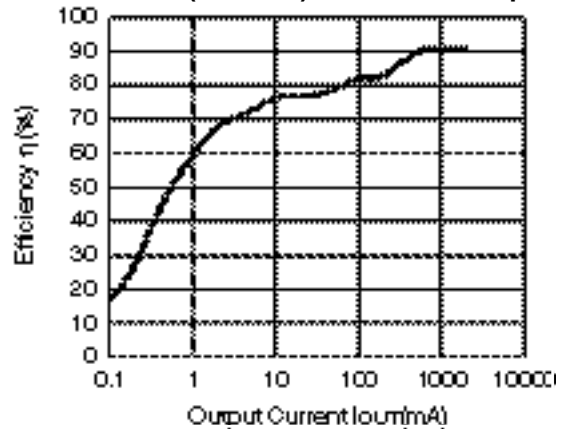
R1224N332E (V_{IN}=7.0V) CDRH127-10μH



R1224N332E (V_{IN}=4.8V) CDRH127-10μH

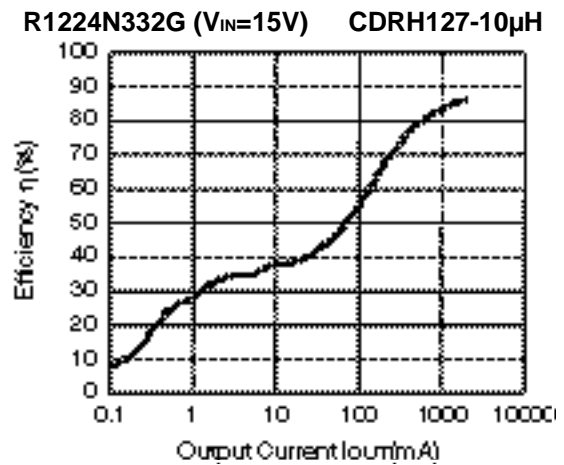
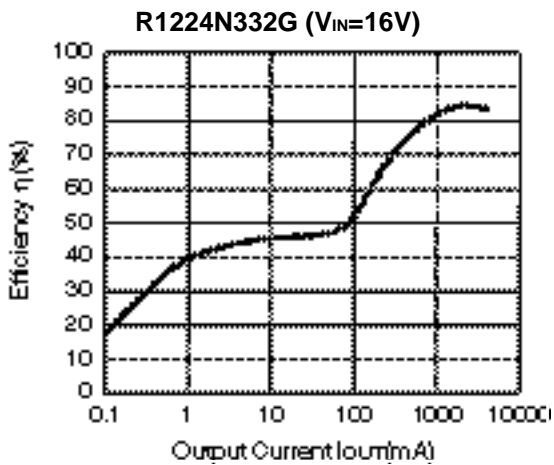
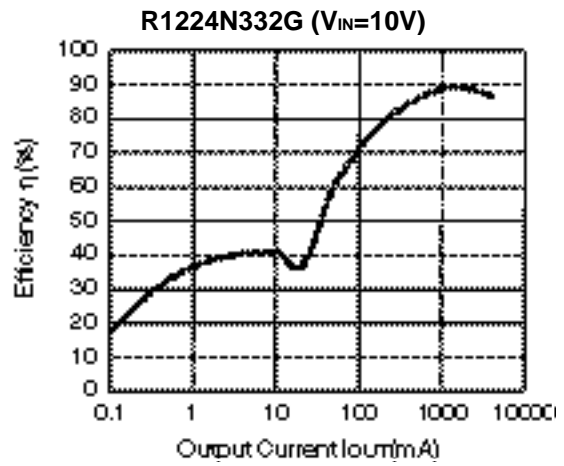
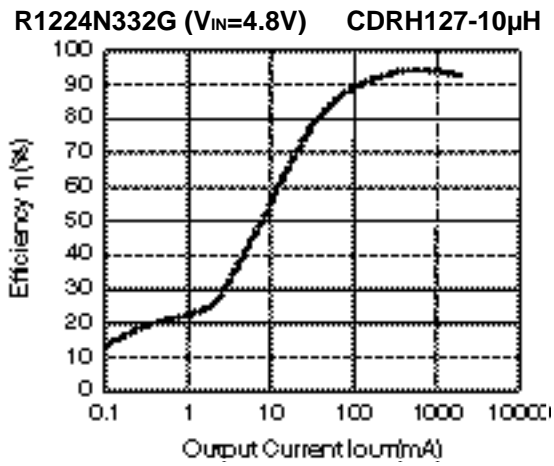
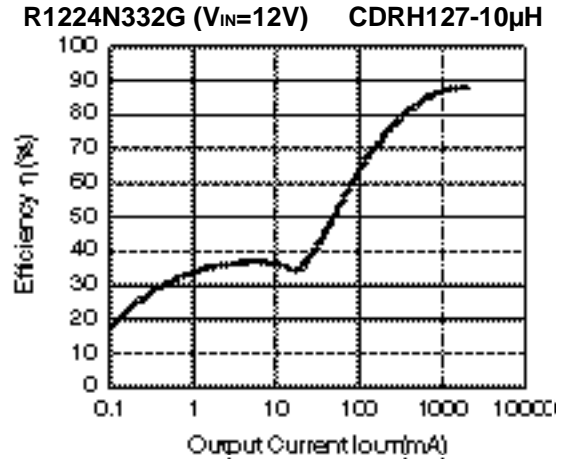
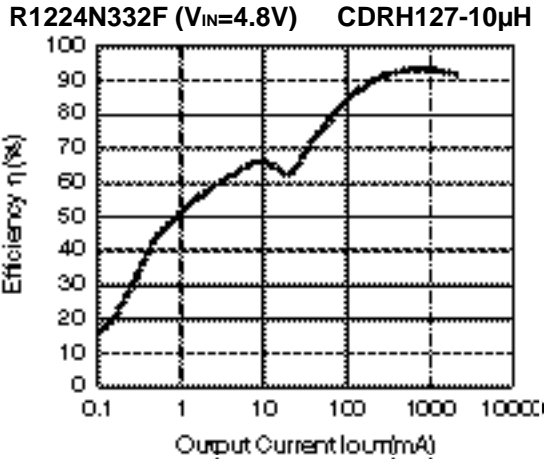


R1224N332F (V_{IN}=7.0V) CDRH127-10μH

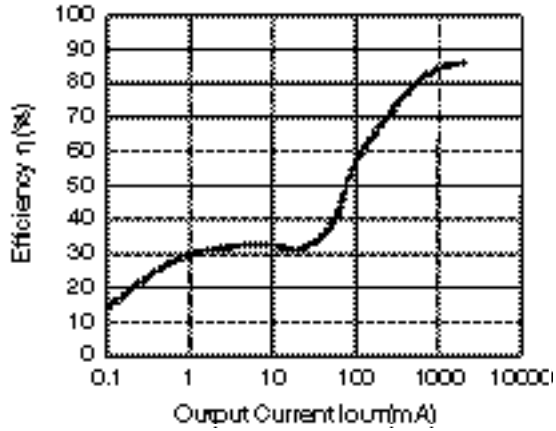


R1224N

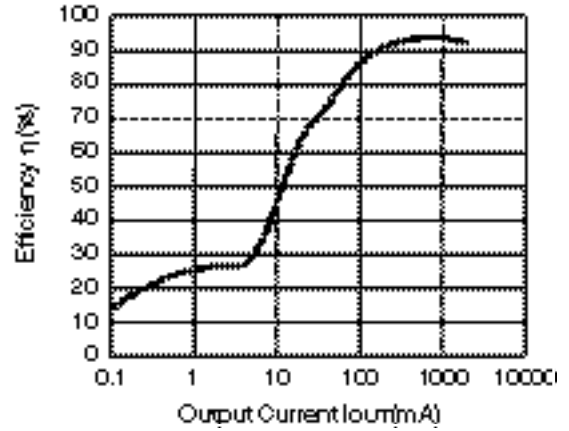
NO.EA-096-181004



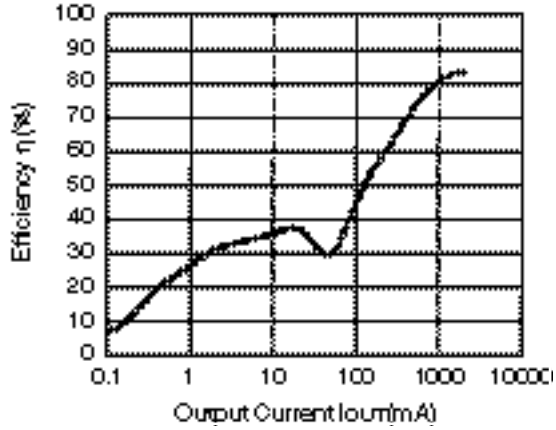
R1224N332H (V_{IN}=12V) CDRH127-10μH



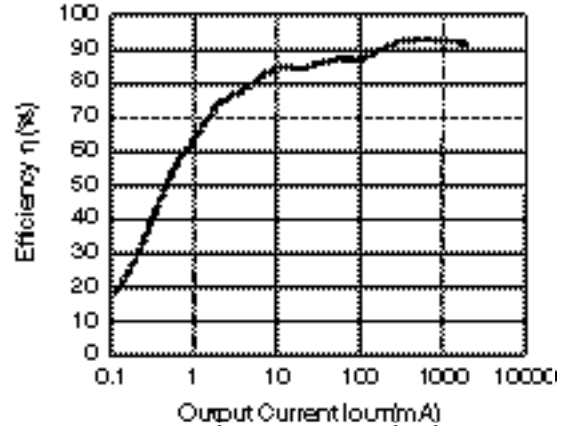
R1224N332H (V_{IN}=4.8V) CDRH127-10μH



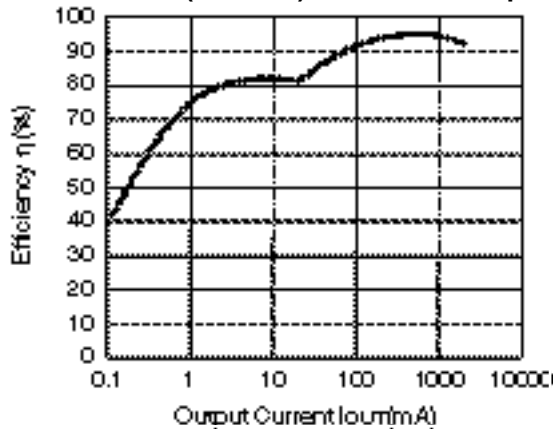
R1224N332H (V_{IN}=15V) CDRH127-10μH



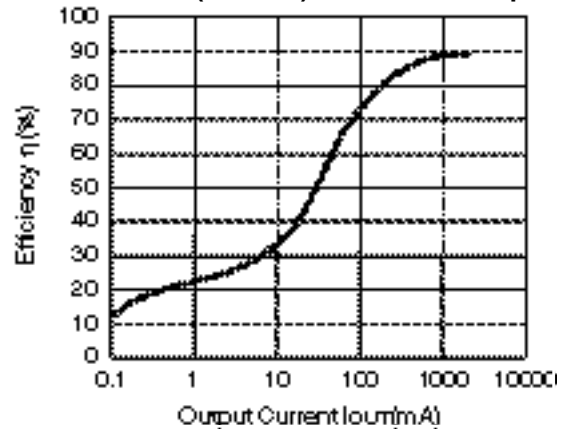
R1224N332L (V_{IN}=7.0V) CDRH127-27μH



R1224N332L (V_{IN}=4.8V) CDRH127-27μH

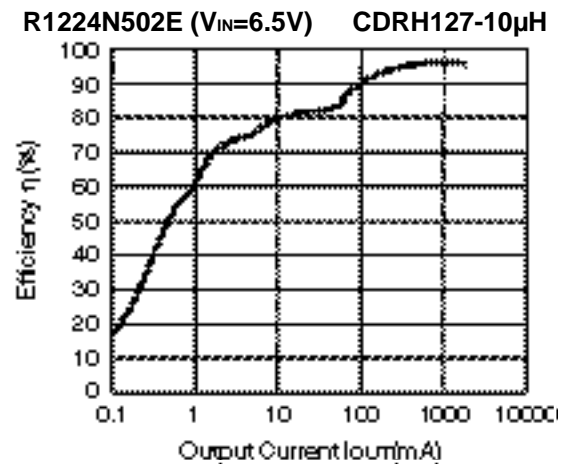
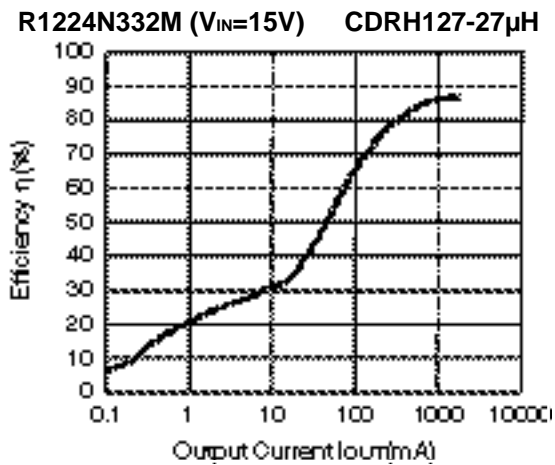
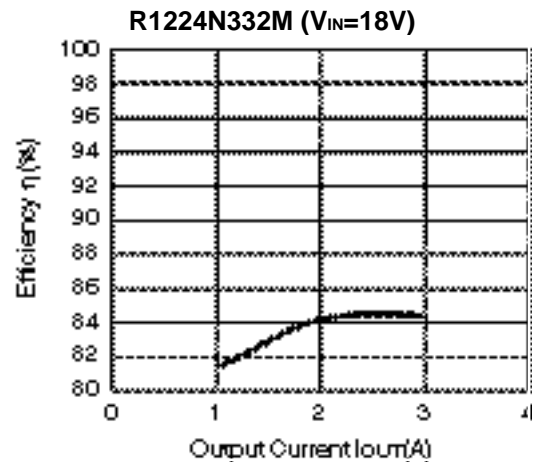
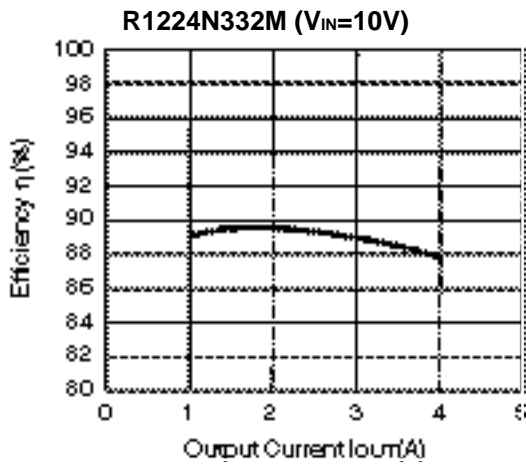
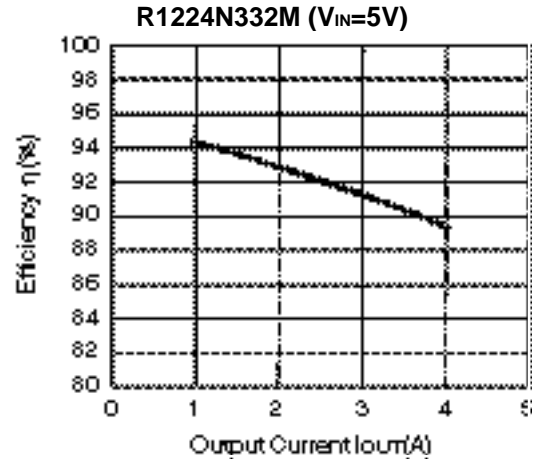
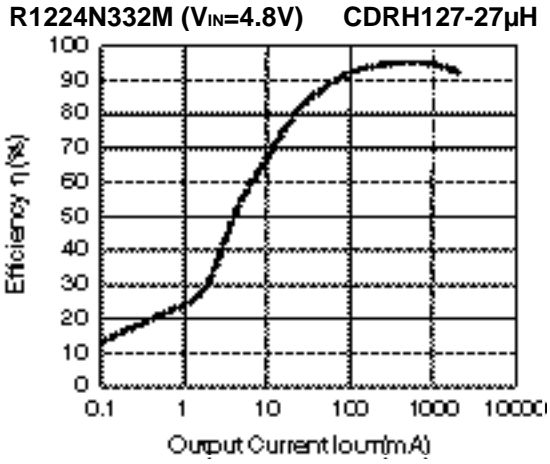


R1224N332M (V_{IN}=12V) CDRH127-27μH

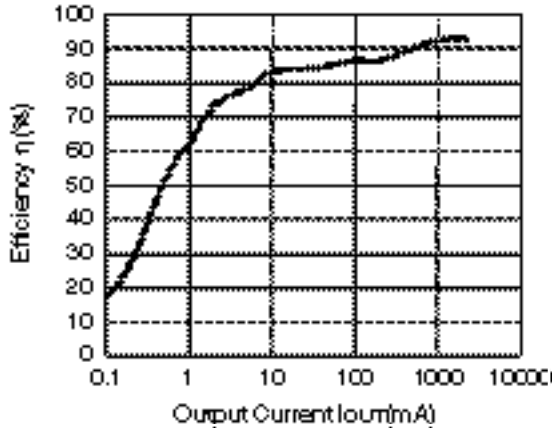


R1224N

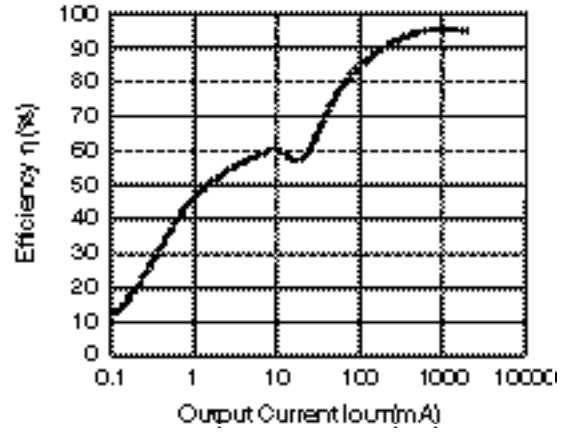
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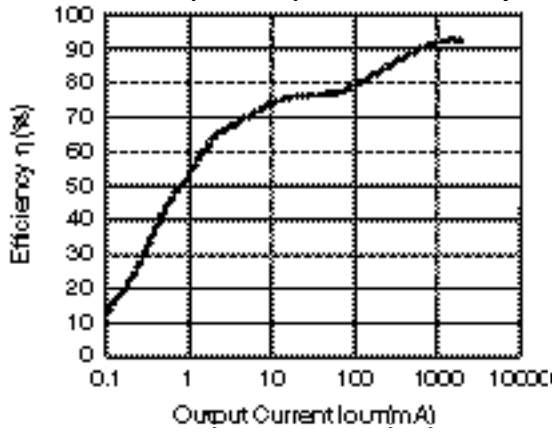
R1224N502E (V_{IN}=10V) CDRH127-10μH



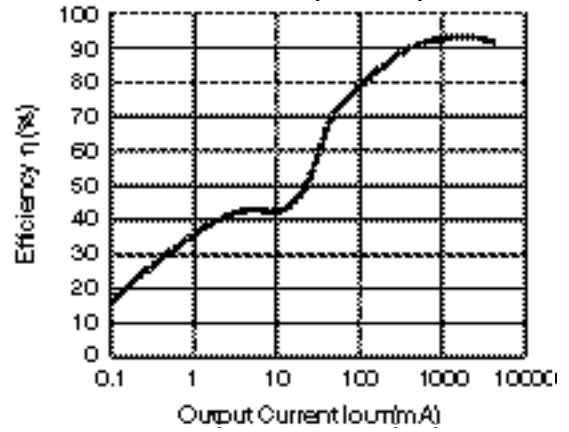
R1224N502F (V_{IN}=6.5V) CDRH127-10μH



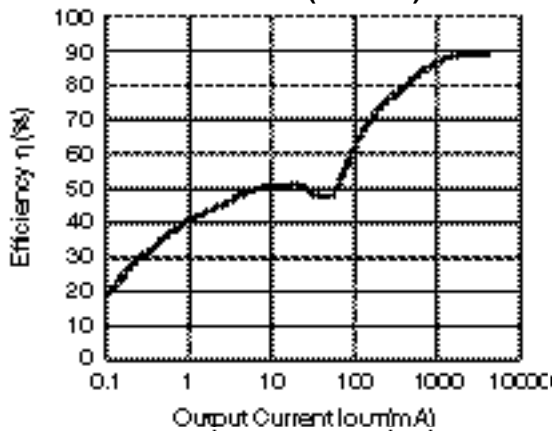
R1224N502F (V_{IN}=10V) CDRH127-10μH



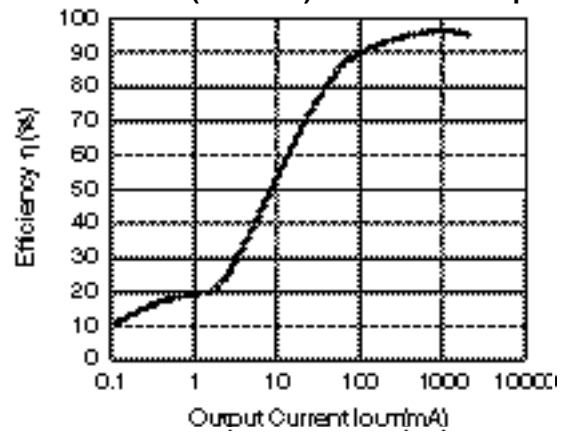
R1224N502G (V_{IN}=10V)



R1224N502G (V_{IN}=16V)



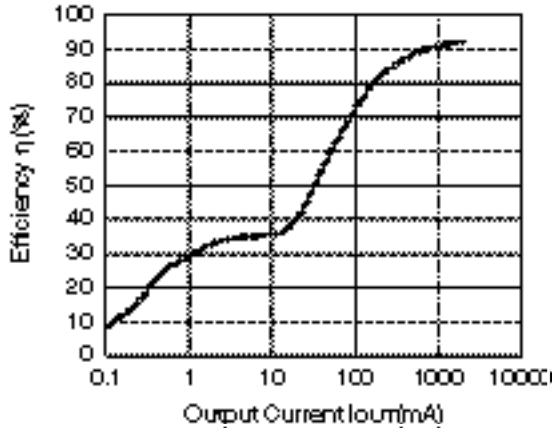
R1224N502G (V_{IN}=6.5V) CDRH127-10μH



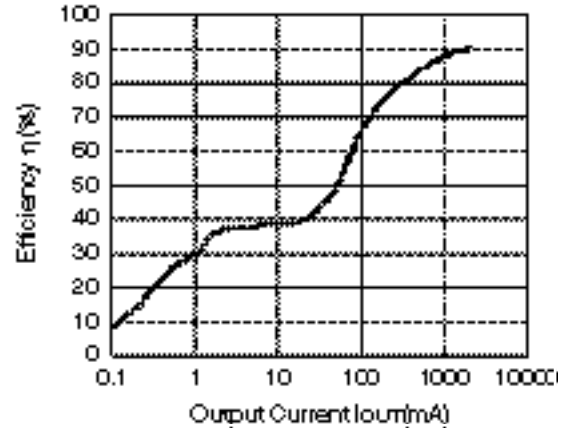
R1224N

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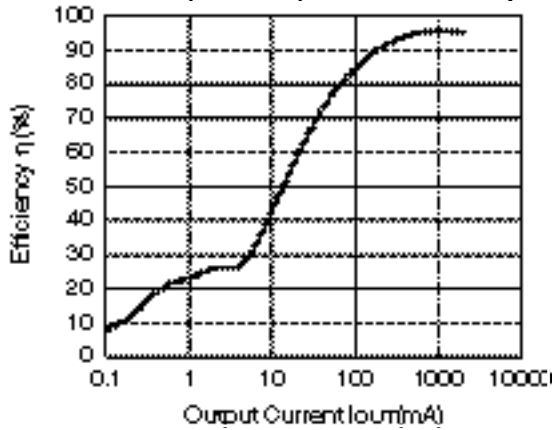
R1224N502G ($V_{IN}=12V$) CDRH127-10 μ H



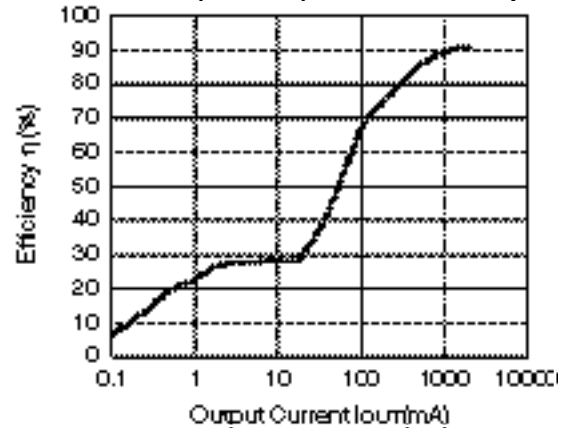
R1224N502G ($V_{IN}=15V$) CDRH127-10 μ H



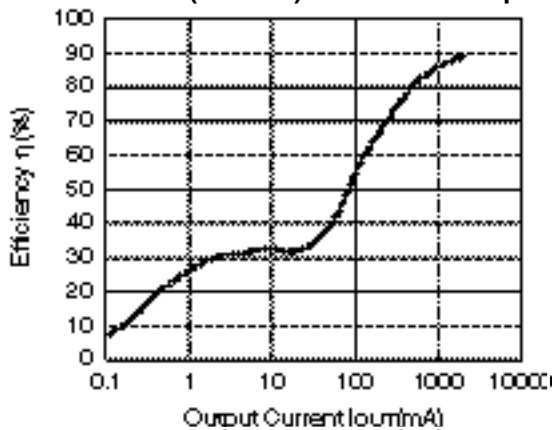
R1224N502H ($V_{IN}=6.5V$) CDRH127-10 μ H



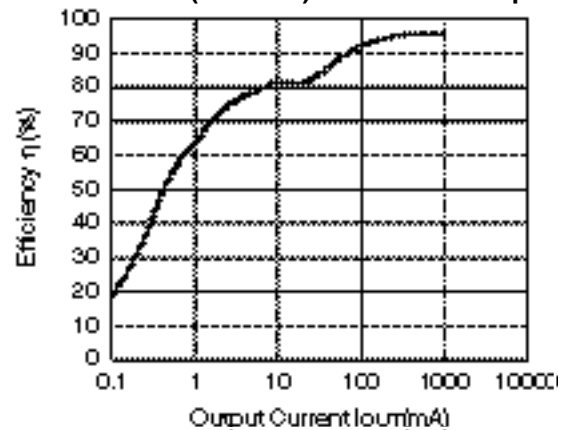
R1224N502H ($V_{IN}=12V$) CDRH127-10 μ H

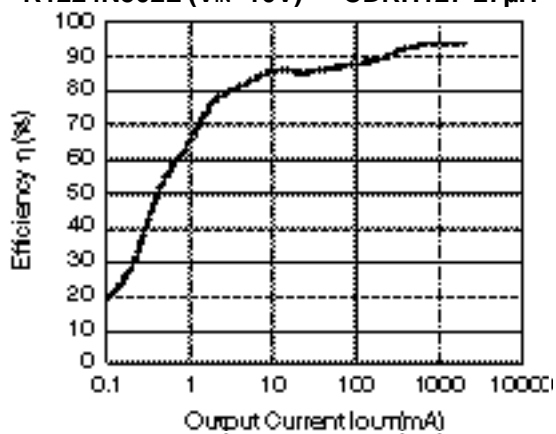
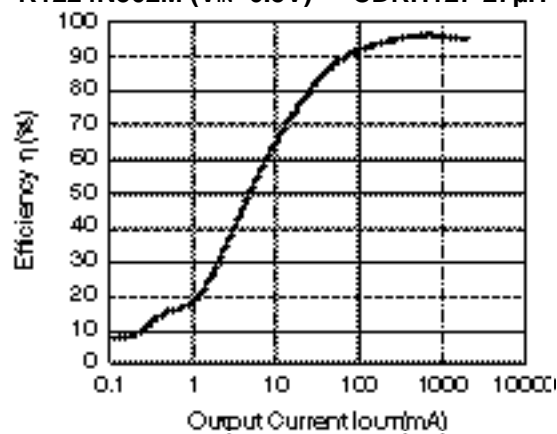
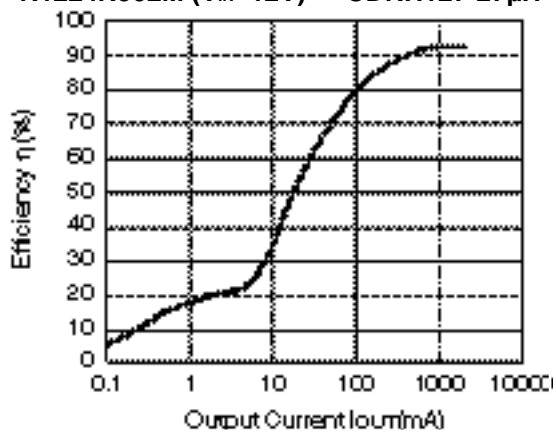
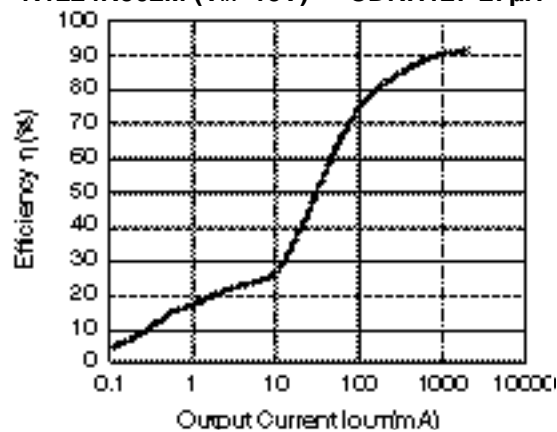


R1224N502H ($V_{IN}=15V$) CDRH127-10 μ H



R1224N502L ($V_{IN}=6.5V$) CDRH127-27 μ H



R1224N502L ($V_{IN}=10V$) CDRH127-27 μ HR1224N502M ($V_{IN}=6.5V$) CDRH127-27 μ HR1224N502M ($V_{IN}=12V$) CDRH127-27 μ HR1224N502M ($V_{IN}=15V$) CDRH127-27 μ H

*Note: Typical characteristics 2) are obtained with using the following components;

PMOS: IRF7406 (IR)

L : CDRH127-100MC (Sumida: 10 μ H)

SD : RB083L-20 (Rohm)

C1 : 25SC47 (Sanyo/OS-con: 47 μ F/25V) \times 2

C2 : 0.1 μ F (Ceramic Type)

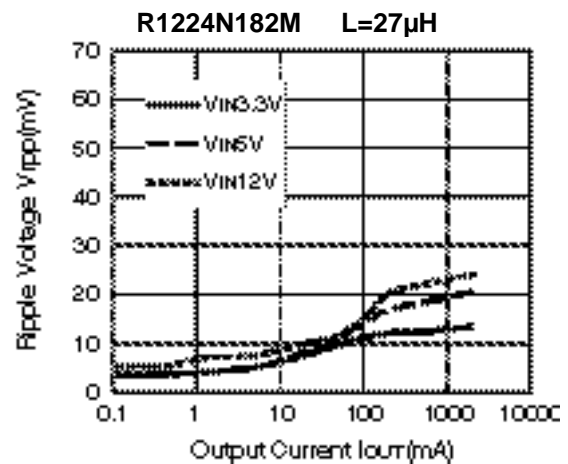
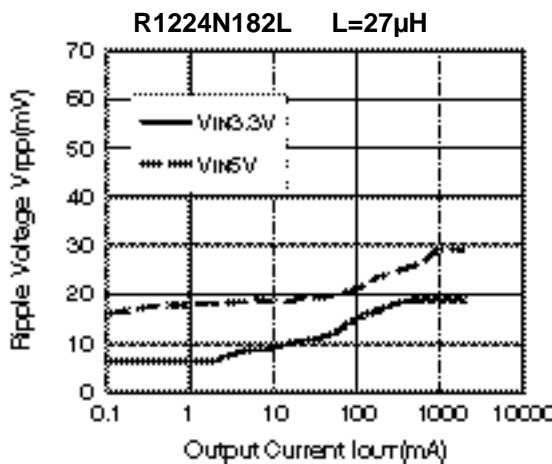
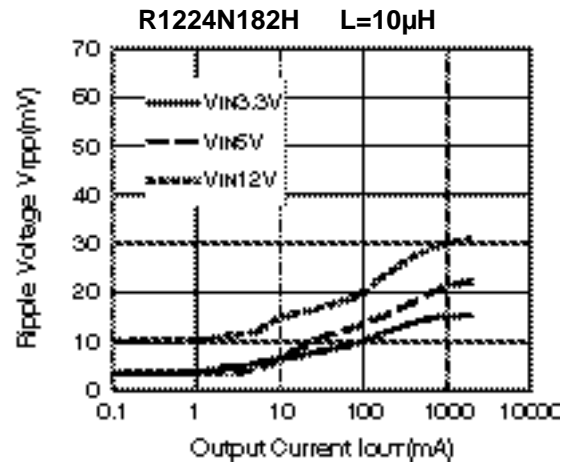
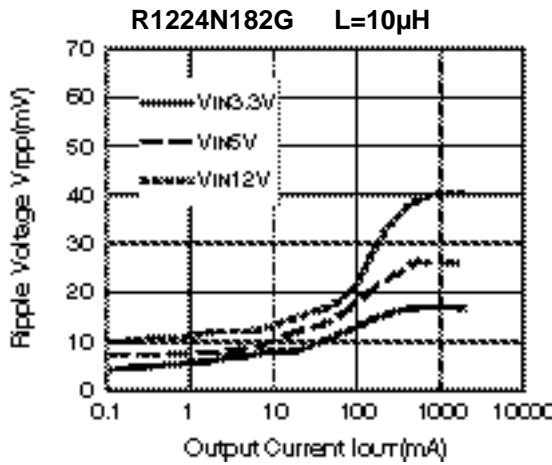
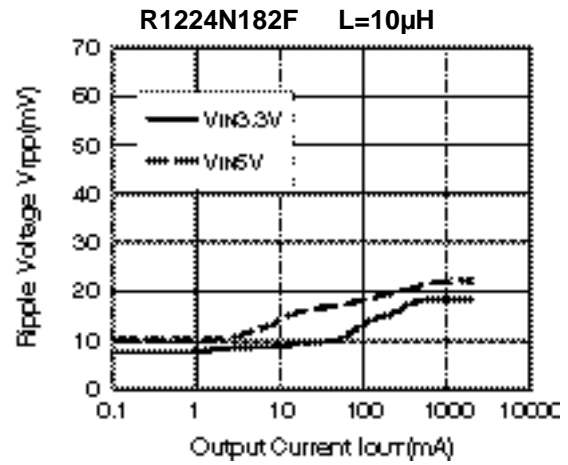
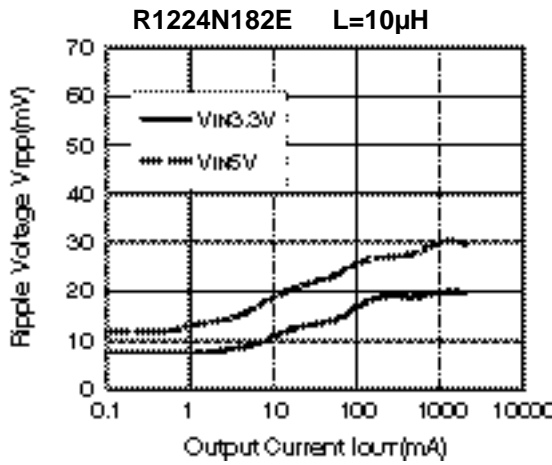
C3 : 10SA220 (Sanyo/OS-con: 220 μ F/10V)

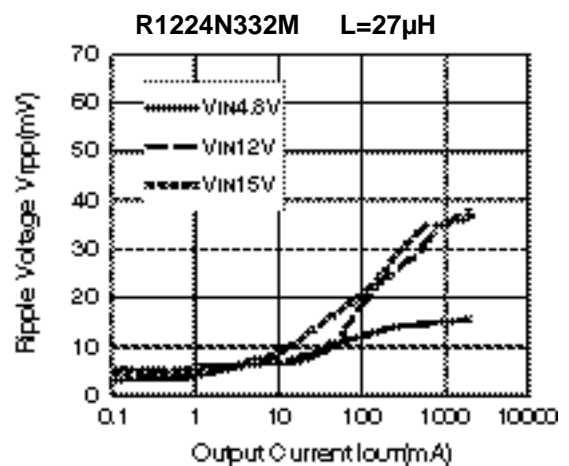
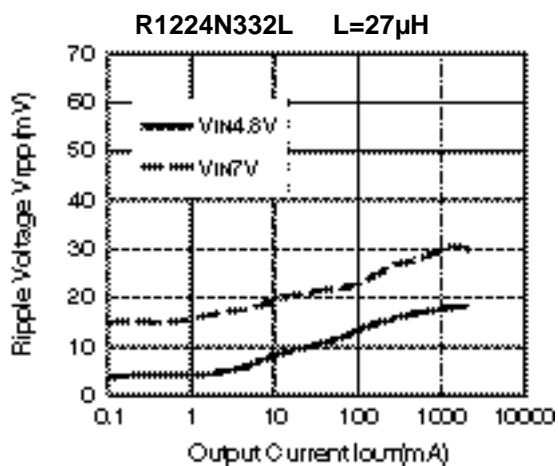
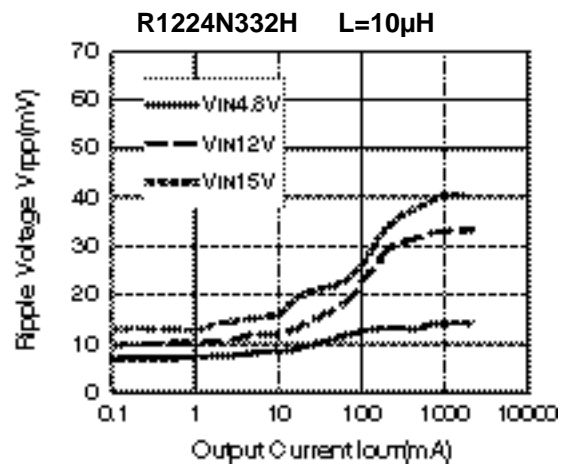
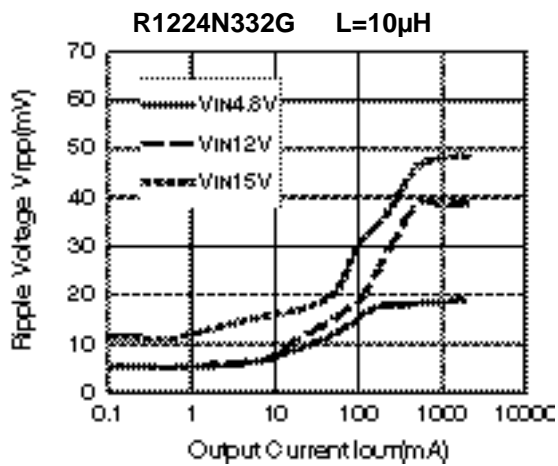
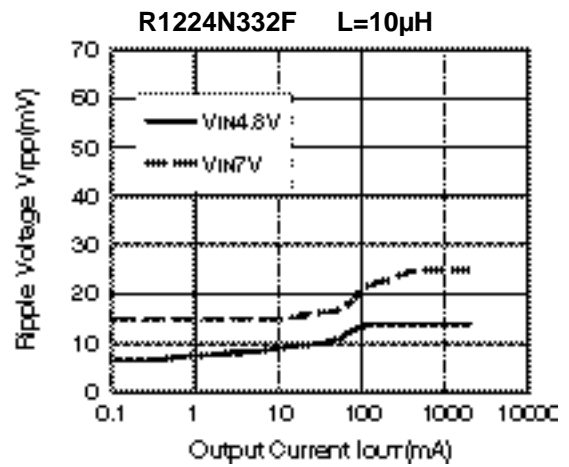
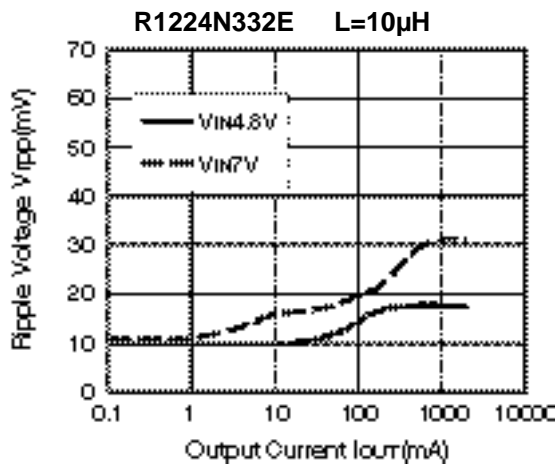
R1 : 10 Ω

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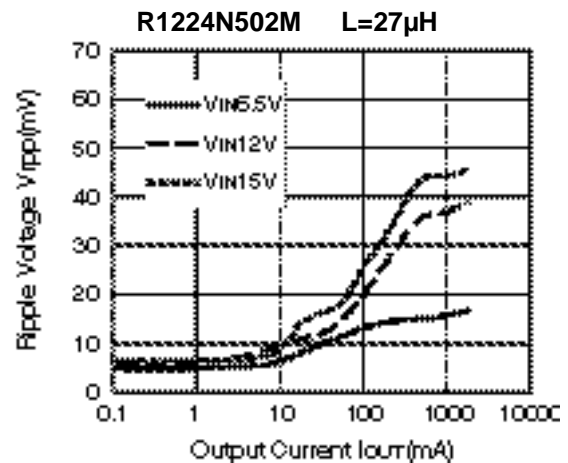
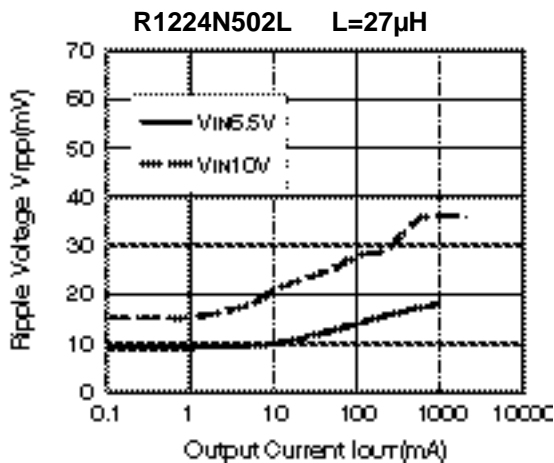
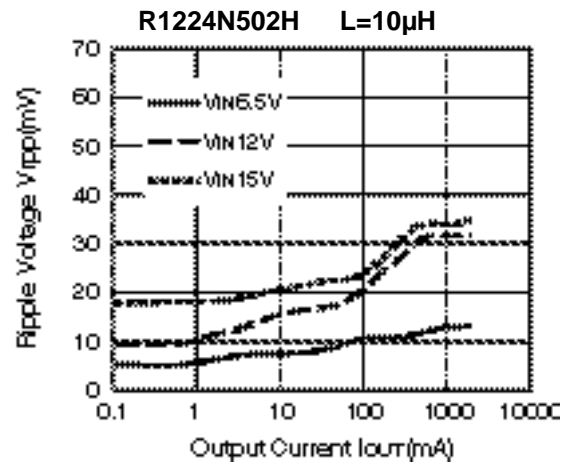
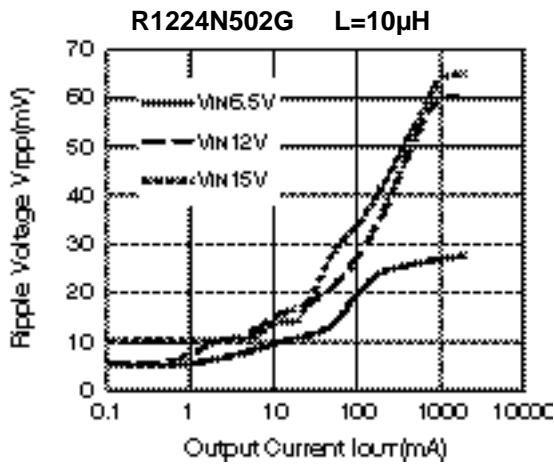
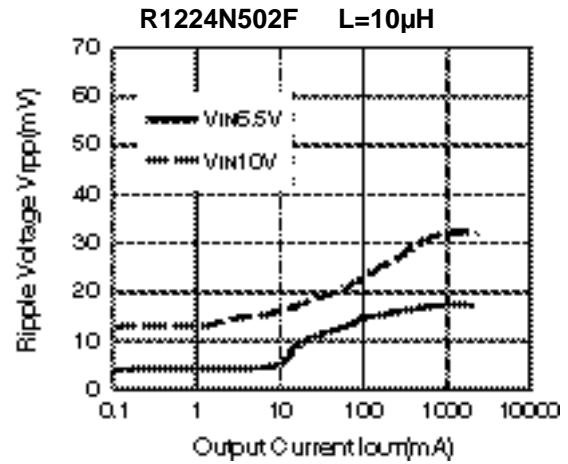
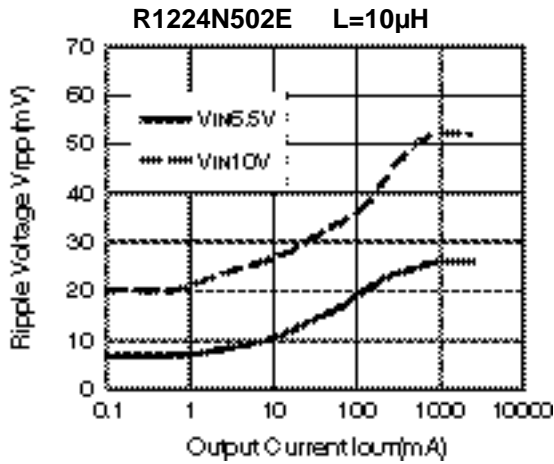
3) Ripple Voltage vs. Output Current



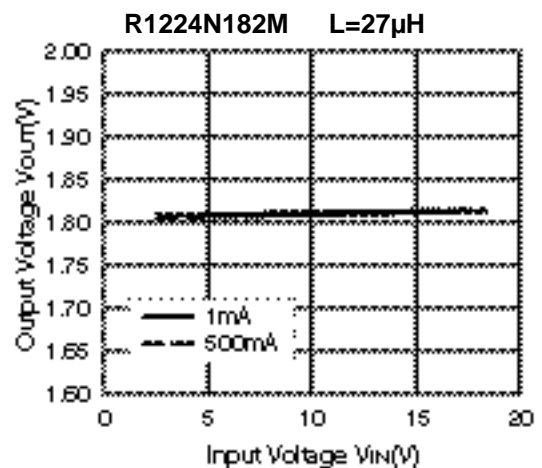
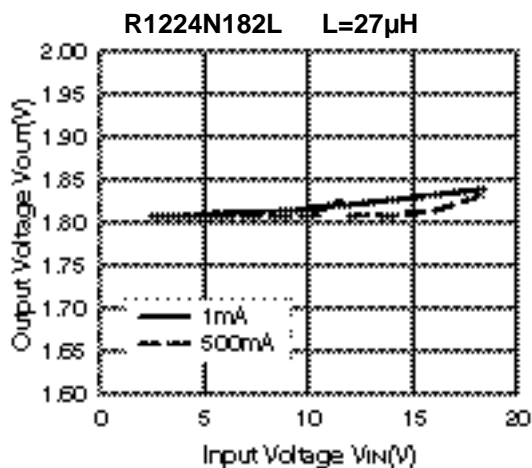
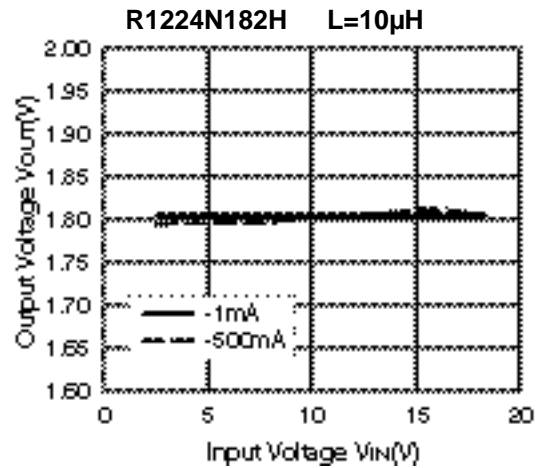
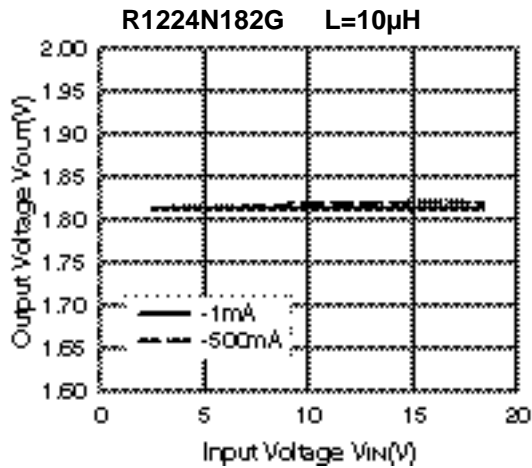
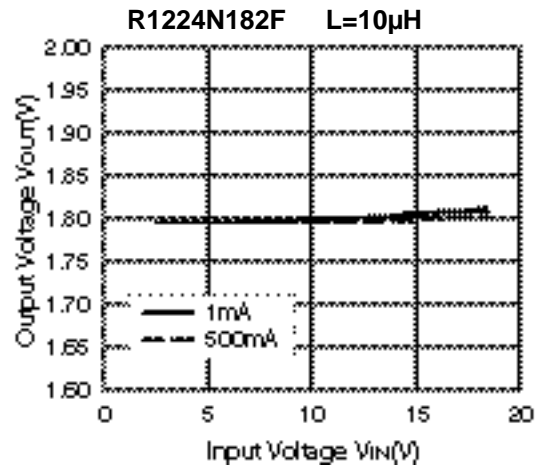
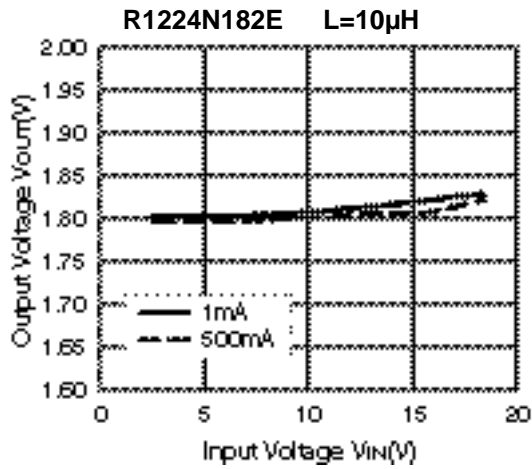


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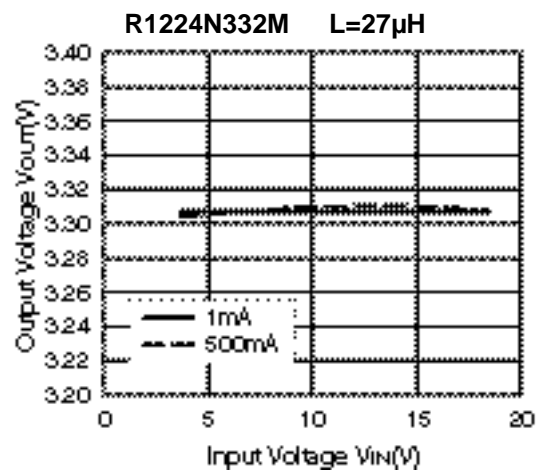
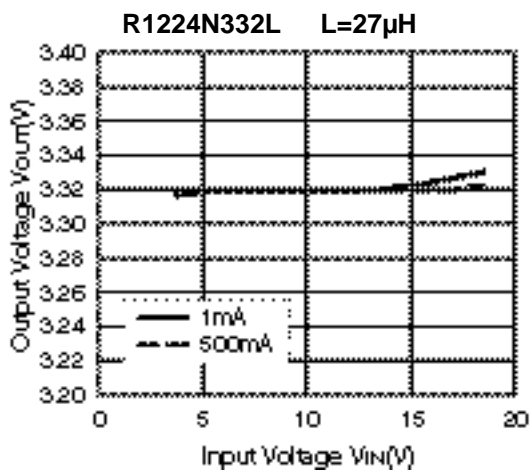
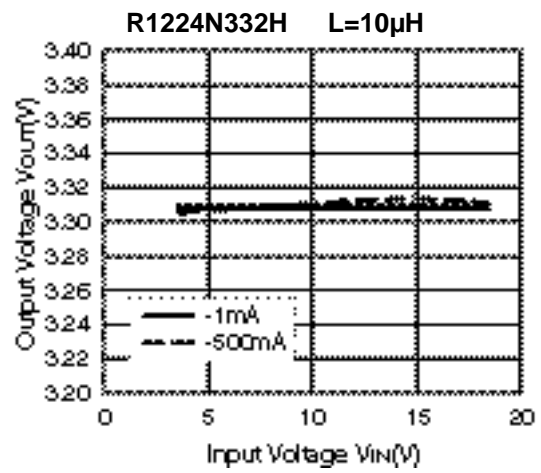
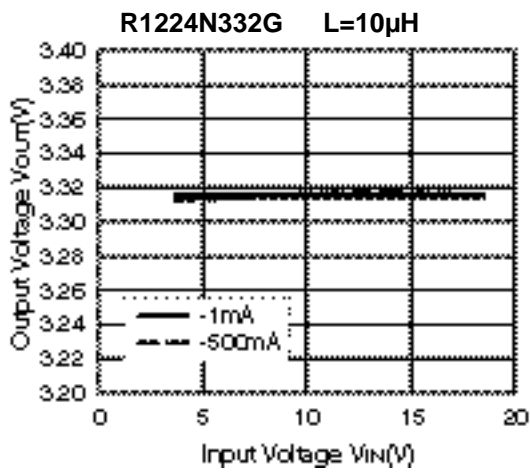
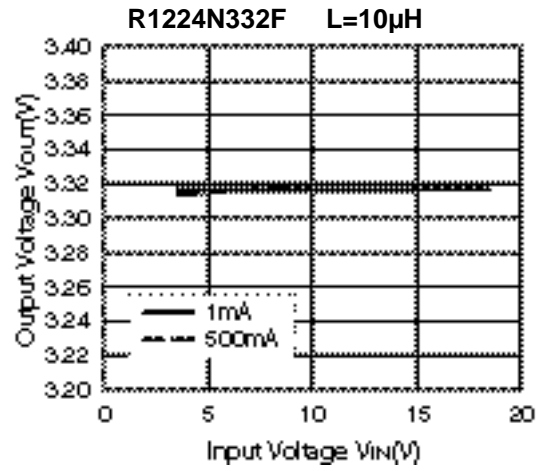
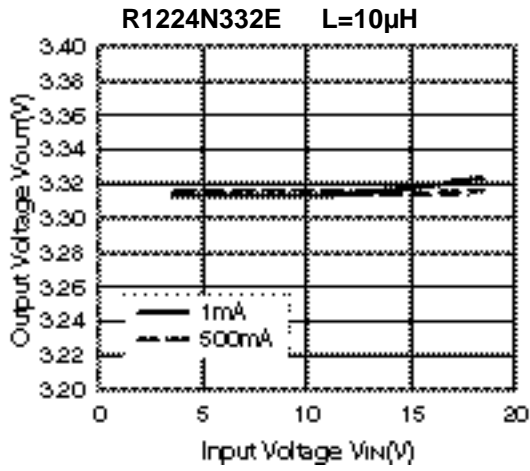


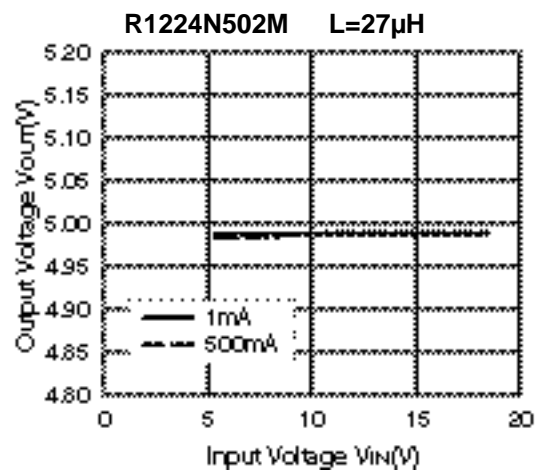
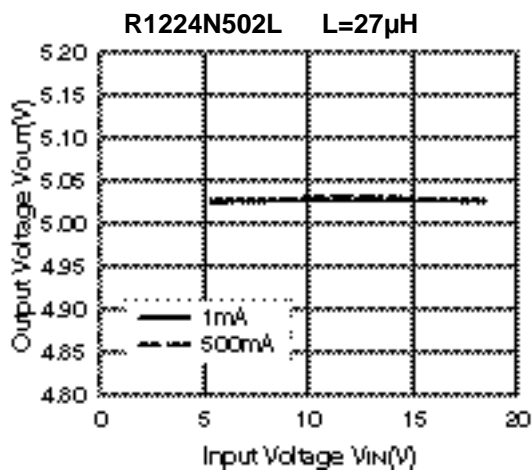
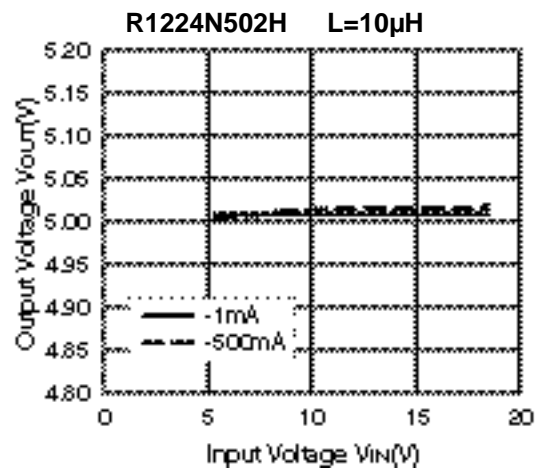
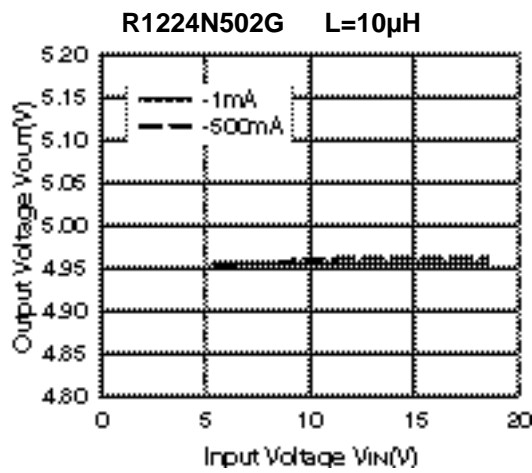
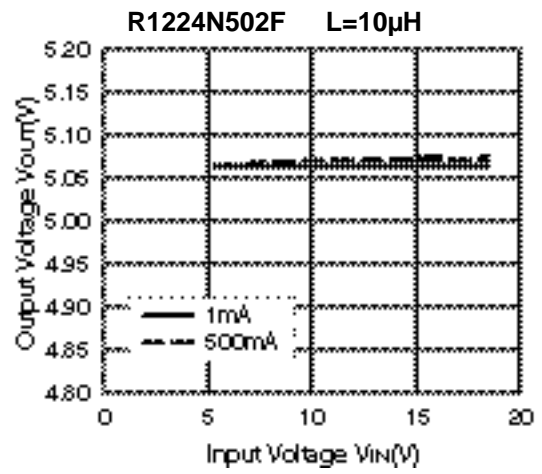
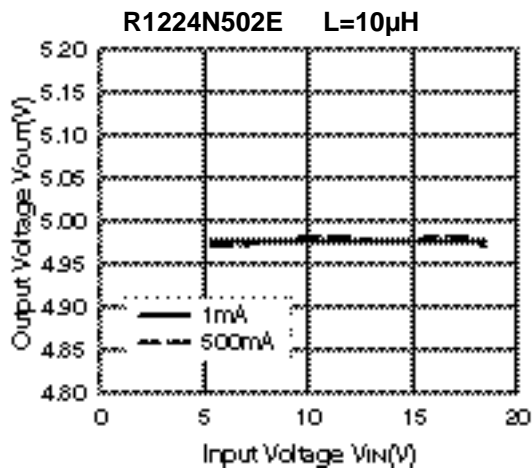
4) Output Voltage vs. Input Voltage



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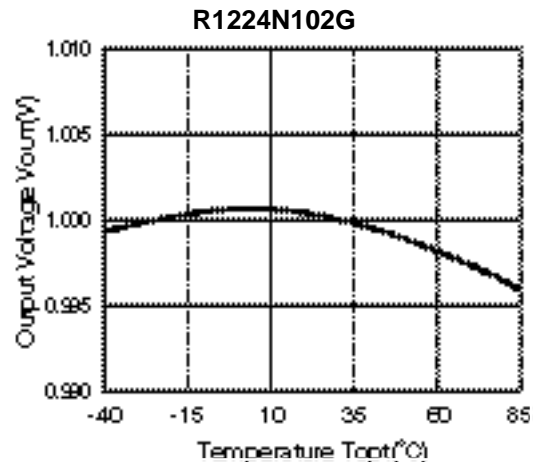
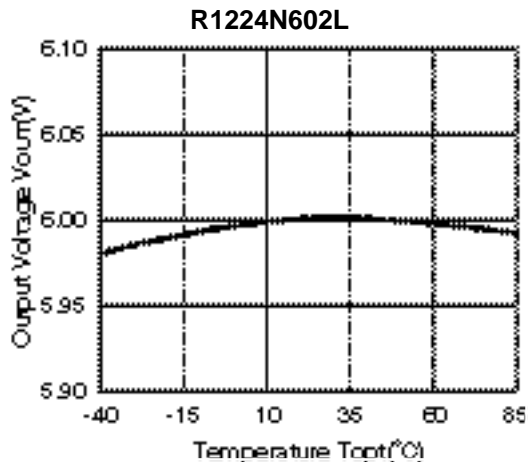
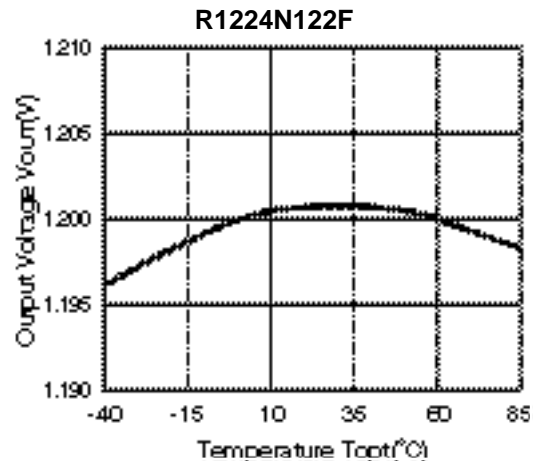
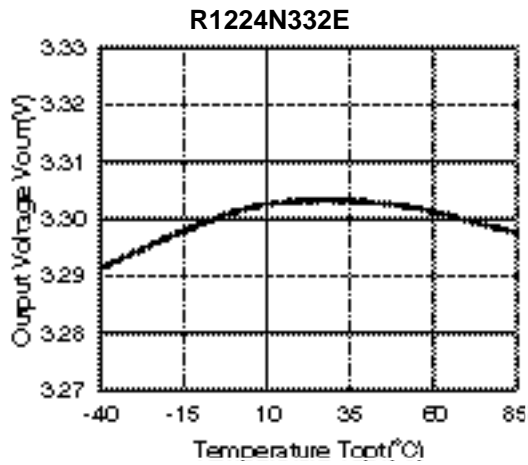




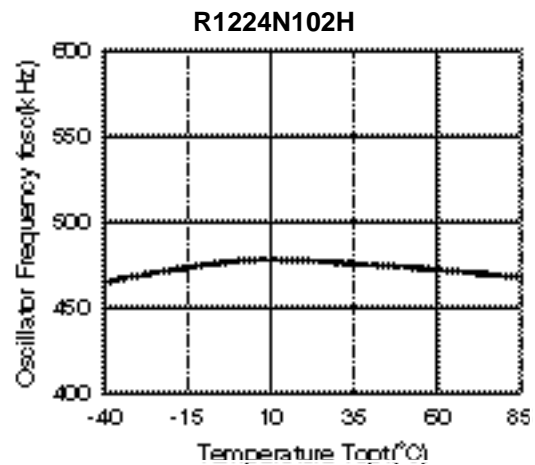
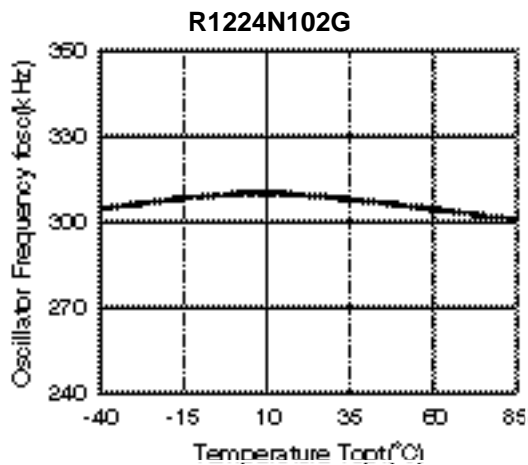
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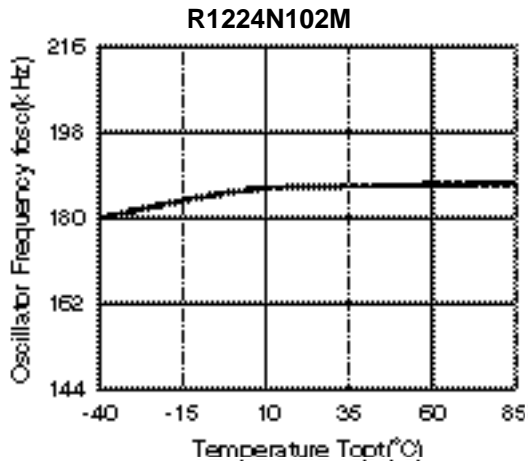
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5) Output Voltage vs. Temperature

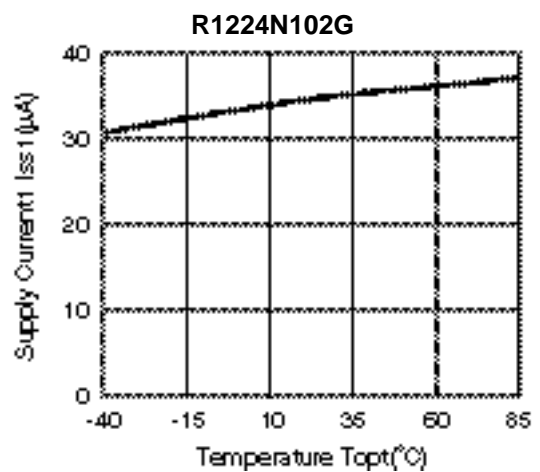
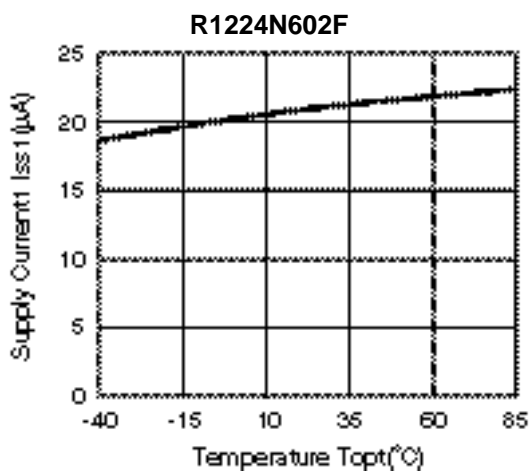
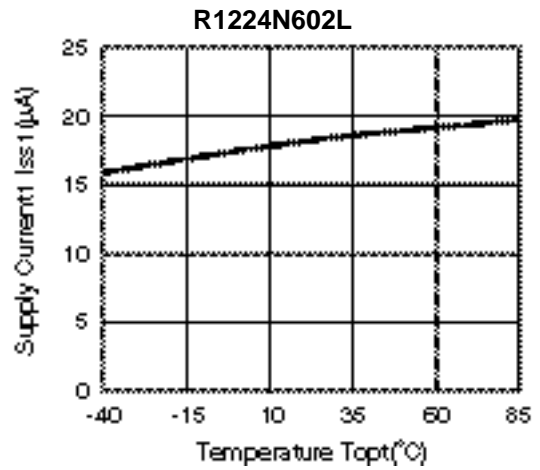
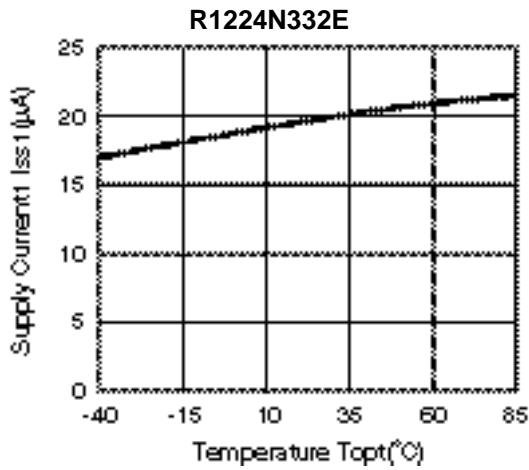


6) Oscillator Frequency vs. Temperature



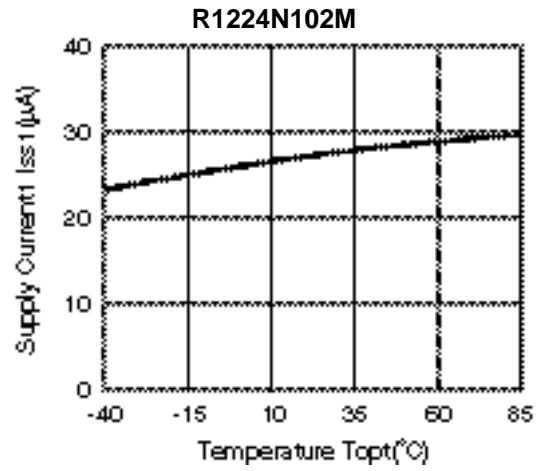
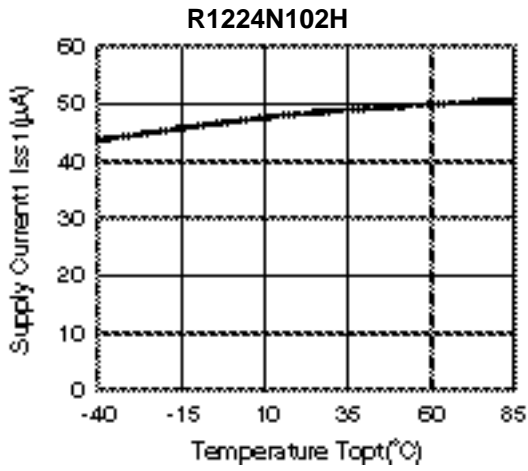


7) Supply Current vs. Temperature

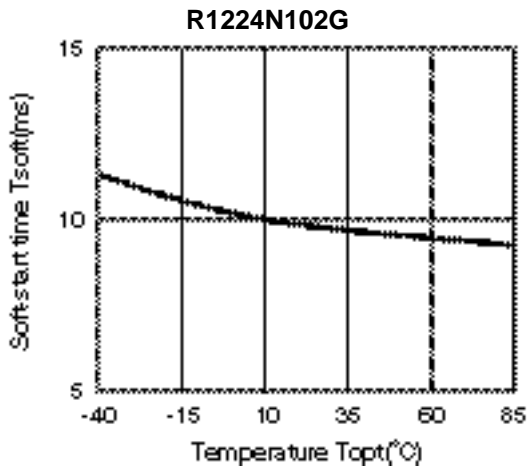


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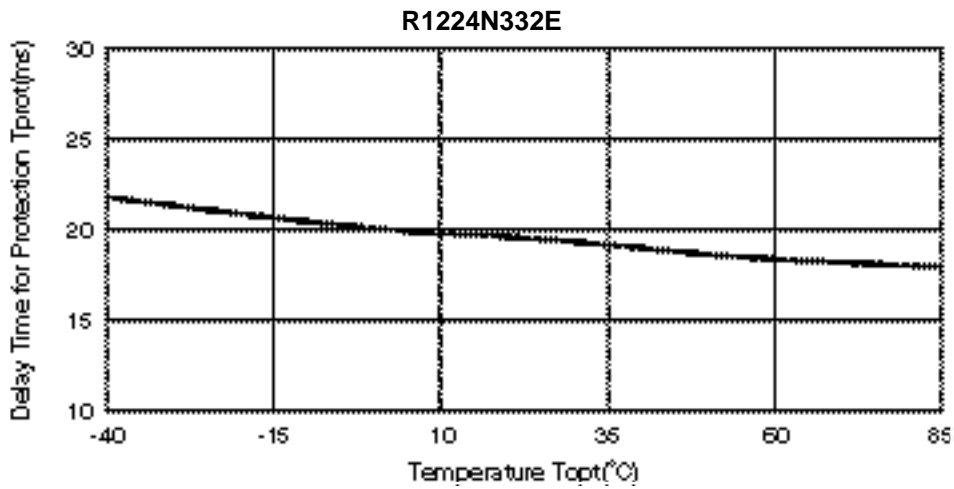
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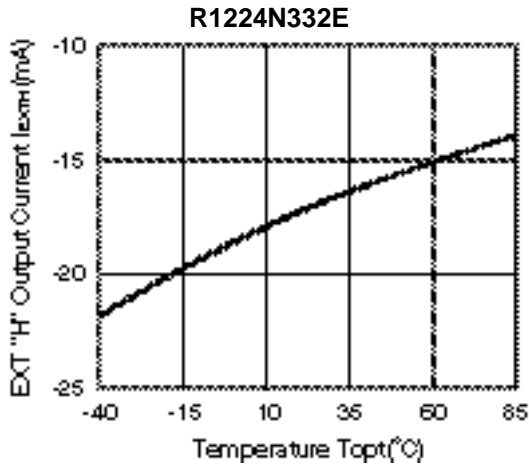
8) Soft-start time vs. Temperature



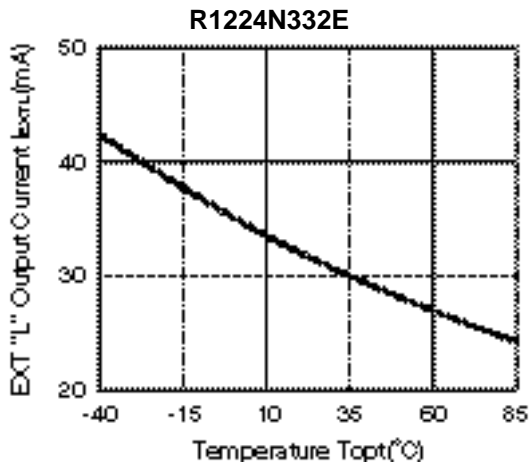
9) Delay Time for Protection vs. Temperature



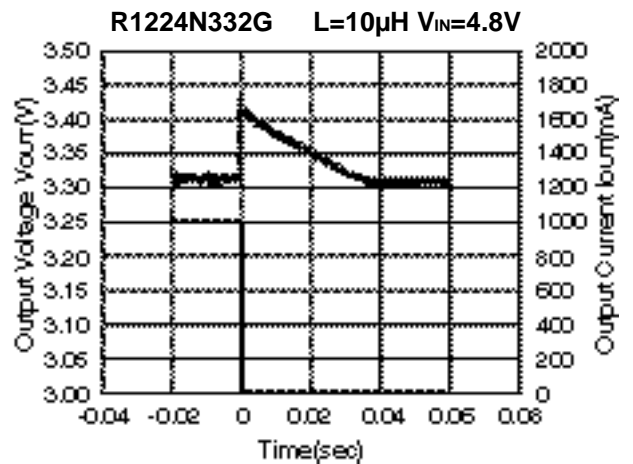
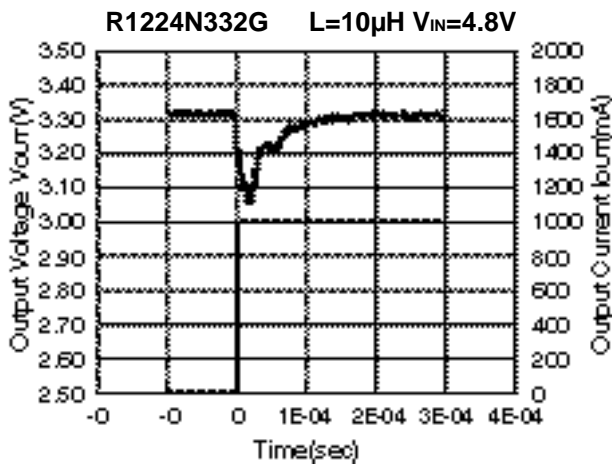
10) EXT "H" Output Current vs. Temperature



11) EXT "L" Output Current vs. Temperature

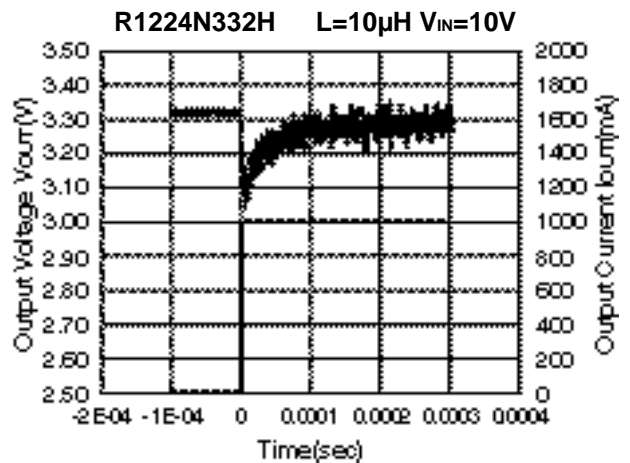
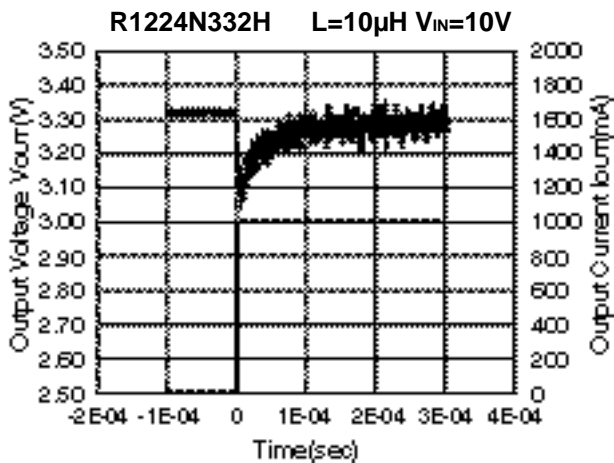
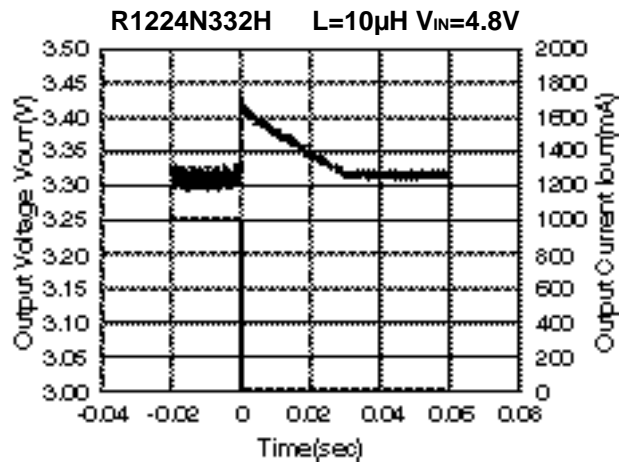
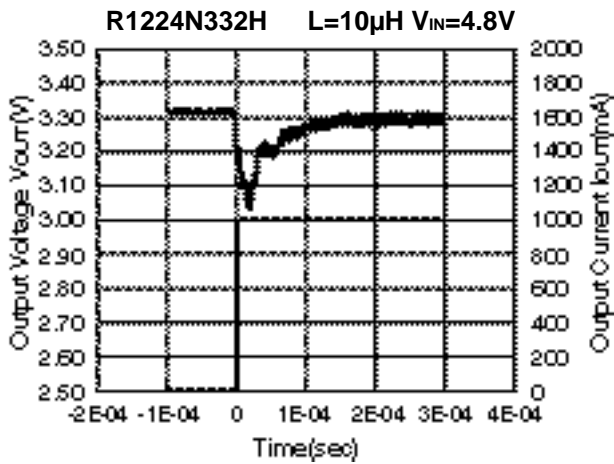
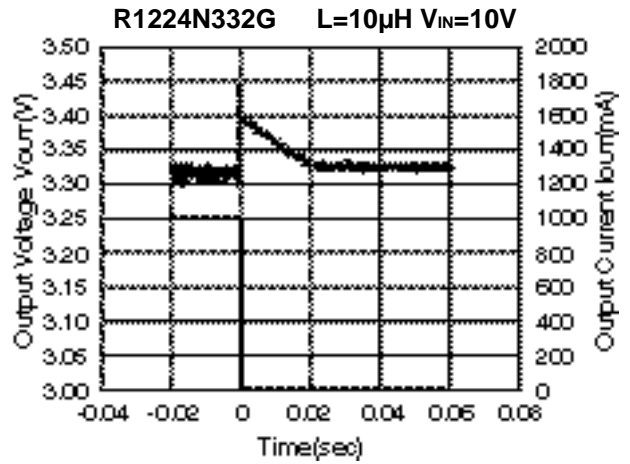
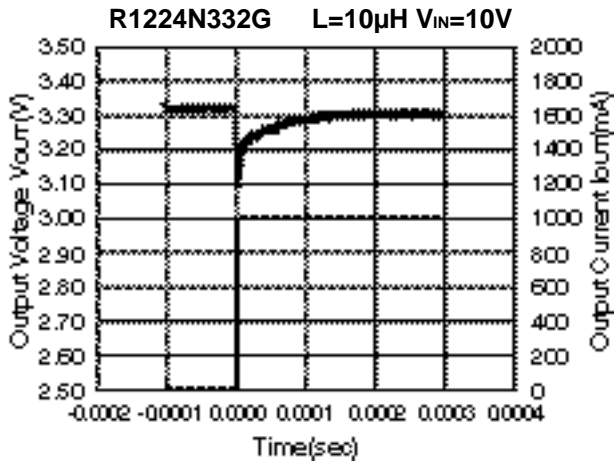


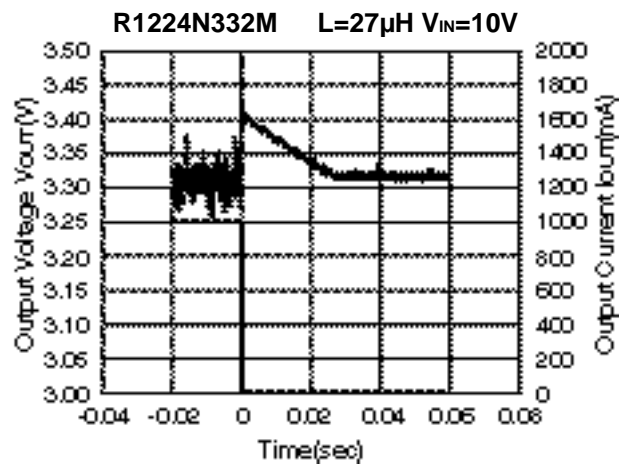
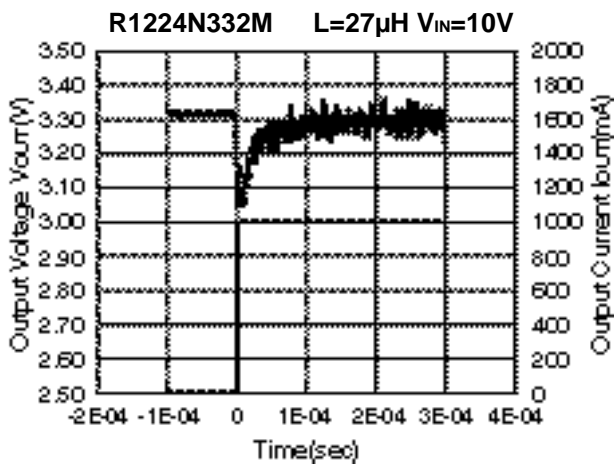
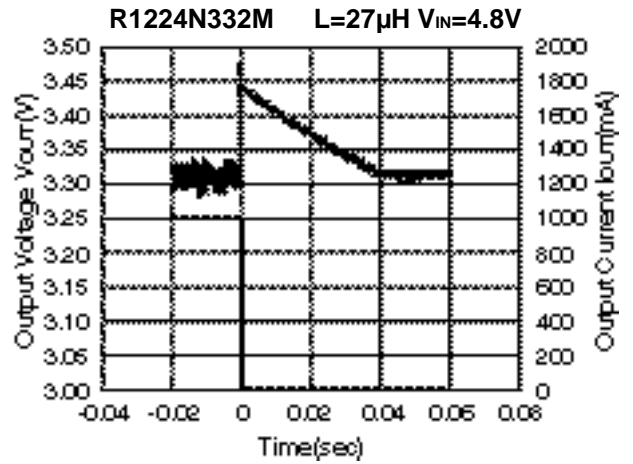
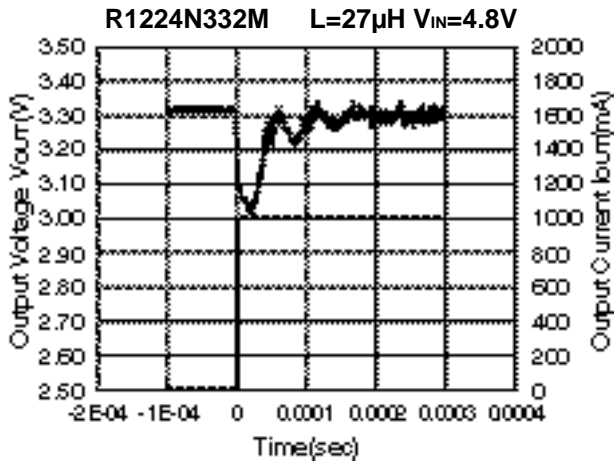
12) Load Transient Response



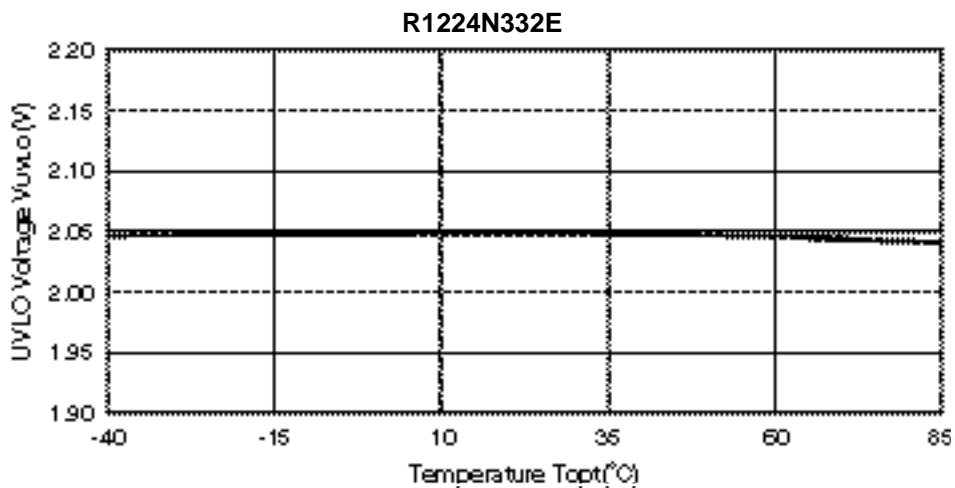
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12) UVLO Voltage vs. Temperature





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9. WLCSP products should be used in light shielded environments. The light exposure can influence functions and characteristics of the products under operation or storage.
10. There can be variation in the marking when different AOI (Automated Optical Inspection) equipment is used. In the case of recognizing the marking characteristic with AOI, please contact our sales or our distributor before attempting to use AOI.
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