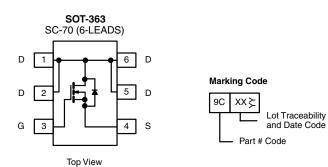
SQ1470EH



Vishay Siliconix

Automotive N-Channel 30 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	30			
$R_{DS(on)} (\Omega)$ at $V_{GS} = 4.5 V$	0.065			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 2.5 V$	0.095			
I _D (A)	2.8			
Configuration	Single			

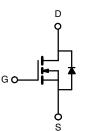


FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- AEC-Q101 Qualified^d
- 100 % $R_{\rm q}$ and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



KOHS COMPLIANT HALOGEN FREE



N-Channel MOSFET

ORDERING INFORMATION	
Package	SC-70
Lead (Pb)-free and Halogen-free	SQ1470EH-T1-GE3

ABSOLUTE MAXIMUM RATINGS	(T _C = 25 °C, unles	s otherwise noted)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 12	v	
Continuous Drain Current ^a	T _C = 25 °C	- I _D -	2.8		
	T _C = 125 °C		2.8		
Continuous Source Current (Diode Conduction) ^a Pulsed Drain Current ^b		I _S	2.8	А	
		I _{DM}	11		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	10		
Single Pulse Avalanche Energy	L = 0.1 MH	E _{AS}	5	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	D	3.3	w	
	T _C = 125 °C	P _D	1.1		
Operating Junction and Storage Temperature F	Range	T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	125	°C/W
unction-to-Foot (Drain)		R _{thJF}	45	0/10

Notes

a. Package limited.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

c. When mounted on 1" square PCB (FR-4 material).

d. Parametric verification ongoing.

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PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static	•						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$		30	-	-	v
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		1.0	1.6	v
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$		-	-	± 500	nA
Zero Gate Voltage Drain Current		$V_{GS} = 0 V$	V _{DS} = 30 V	-	-	1	μA
	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 30 V, T _J = 125 °C	-	-	50	
		$V_{GS} = 0 V$	V _{DS} = 30 V, T _J = 175 °C	-	-	150	
On-State Drain Current ^a	I _{D(on)}	$V_{GS} = 4.5 V$	$V_{DS} \ge 5 V$	5	-	-	Α
		$V_{GS} = 4.5 V$	I _D = 3.8 A	-	0.050	0.065	Ω
Duain Course On Otata Desistance?		$V_{GS} = 4.5 V$	I _D = 3.8 A, T _J = 125 °C	-	-	0.097	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 V$	I _D = 3.8 A, T _J = 175 °C	-	-	0.115	
		V _{GS} = 2.5 V	I _D = 3.1 A	-	0.070	0.095	
Forward Transconductanceb	9 _{fs}	V _{DS}	= 15 V, I _D = 2 A	-	8	-	S
Dynamic ^b	•	·			•		
Input Capacitance	C _{iss}		V _{GS} = 0 V V _{DS} = 25 V, f = 1 MHz	-	488	610	pF
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	60	75	
Reverse Transfer Capacitance	C _{rss}	1		-	36	45	
Total Gate Charge ^c	Qg		= 4.5 V V _{DS} = 15 V, I _D = 3.8 A	-	4.4	6.6	nC
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 4.5 V		-	1	-	
Gate-Drain Charge ^c	Q _{gd}			-	1	-	
Gate Resistance	R _g	f = 1 MHz		3	6.35	9.7	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	8	12	
Rise Time ^c	t _r	V _{DD} =	$V_{DD} = 15 \text{ V}, \text{ R}_1 = 3.9 \Omega$		13	20	- ns
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 3.8$ Å, $V_{GEN} = 4.5$ V, $R_g = 1 \Omega$		-	14	21	
Fall Time ^c	t _f			-	8	12	
Source-Drain Diode Ratings and Char	acteristics ^b	•		•			
Pulsed Current ^a	I _{SM}			-	-	11	Α
	V _{SD}	I _F = 2.5 A, V _{GS} = 0 V		-	0.8	1.2	V

Notes

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

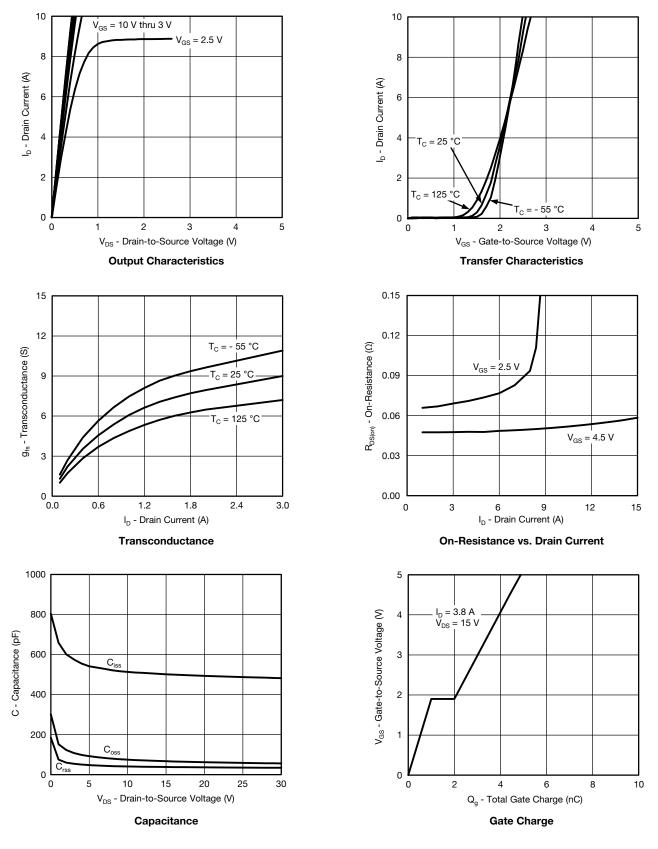
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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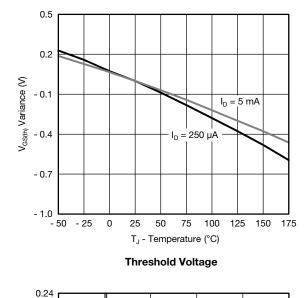
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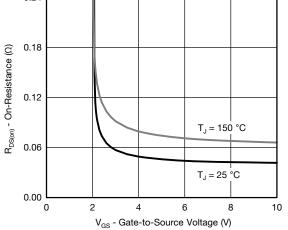
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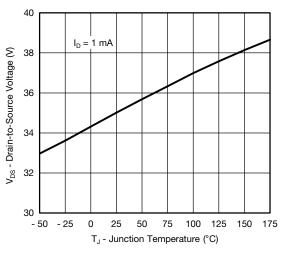
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



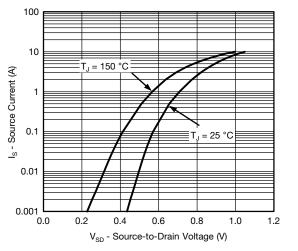
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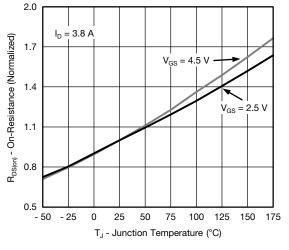
On-Resistance vs. Gate-to-Source Voltage



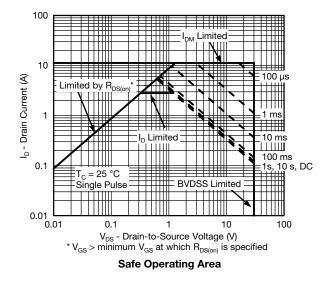
Drain Source Breakdown vs. Junction Temperature



Source Drain Diode Forward Voltage



On-Resistance vs. Junction Temperature



Document Number: 67059

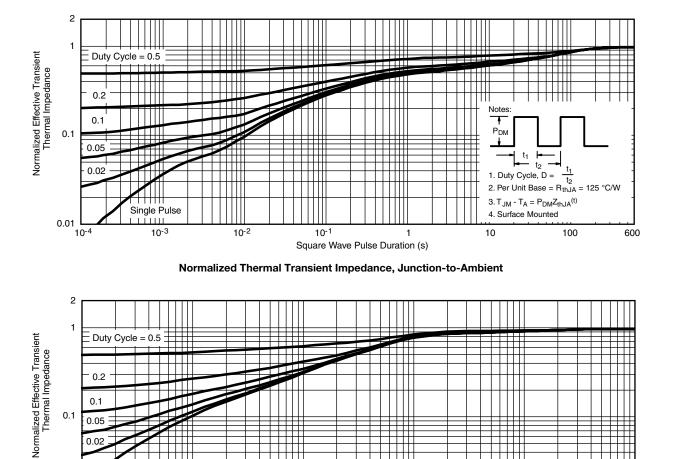
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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)





The characteristics shown in the two graphs

Sinale Pulse

0.05 0.02

0.01 10-4

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

10⁻³

- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Foot

10-1

1

10⁻²

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