PCN Number:	20200206007	PCN Date:	Feb	18, 202	0	
Title: Datasheet for	or MSP430FR2111, MSP430FR2110, MSP430		130FR	0FR2100, MSP430FR2000		
Customer Contact:	PCN Manager		De	Dept: Quality Service		
Change Type:						
Assembly Site	Desi	gn		Wafer	⁻ Bump Site	
Assembly Process	Assembly Process 🛛 Data			Wafer Bump Material		
Assembly Materials Part r		number change		Wafer Bump Process		
Mechanical Specifi					Wafer Fab Site	
Packing/Shipping/	Labeling Test	Process	$\parallel \square$		Fab Materials	
				Wafer	Fab Process	
-		cation Details				
Description of Change						
Texas Instruments Inc				otificatio	on.	
The product datasheet			۷.			
The following change h	listory provides furthe	er detalls.				
TEXAS	MSP430	FR2111, MSP430FR2	110, M	ISP430F	R2100, MSP430FR2000	
INSTRUMENTS		SL	ASE78D	-AUGUST 2	2016-REVISED DECEMBER 2019	
Changes from August 30, 20	18 to December 10, 2019				Page	
Changed the note that	begins "Supply voltage cha	ingos fastor than 0.2 \//us	can tri	agor o BC	Prosot "in	
	ended Operating Conditions					
	gins "TI recommends that p					
Section 5.3, Recomme	nded Operating Conditions				13	
 Added the note that be 	gins "A capacitor tolerance	of ±20% or better is requi	red"	in Section	5.3,	
Recommended Operation	ting Conditions	5.0. Tradical Observation			<u></u>	
	ons 5.8 and 5.10 in Section ISP430 32-kHz Crystal Osc					
	Oscillator (Low Frequency)					
 Changed the note that 	begins "Requires external of	capacitors at both termina	ls" in	Table 5-3	, XT1 Crystal	
Oscillator (Low Freque	ncy)					
 Added the t_(int) parame 	ter in Table 5-9, Digital Inpu	its				
 Added the t_{TB,cap} paran 	neter in Table 5-12, Timer_E	B			<u></u>	
	er symbol from R _I to R _{I,MUX} in ditions for the R _{I.MUX} parame					
Conditions						
	e of 34 k Ω in Table 5-18, Al	DC, Power Supply and Inj	out Rar	nge Condi	tions <u>30</u>	
	ernal ADCCLK source in Ta					
 Added formula for R₁ in Added the note that he 	Table 5-19, ADC, 10-Bit Ti	iming Parameters				
• Added the note that begins "t _{Sample} = $\ln(2^{n+1}) \times \tau$ " in Table 5-19, <i>ADC</i> , 10-Bit Timing Parameters						
 Removed the description of "±3°C" in table note that starts "The device descriptor structure" of Table 5-20, ADC, 10-Bit Linearity Parameters						
Corrected bitfield from IRDSEL to IRDSSEL in Section 6.11.8, Timers (Timer0_B3), in the description that starts						
"The interconnection of Timer0 B3 "						
	Corrected the ADCINCHx column heading in Table 6-14, ADC Channel Connections					
Added P2SELC information in Table 6-26, Port P1, P2 Registers (Base Address: 0200h)						
The datasheet number	will be changing.				–	
Device Family		Change From:		Chang		
MSP430FR2111, MSP	430FR2110,	SLASE78C		SLASE	78D	
MSP430FR2100, MSP	430FR2000					
These changes may be reviewed at the datasheet links provided.						
http://www.ti.com/product/MSP430FR2000						
Reason for Change:						
To accurately reflect device characteristics.						

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative): No anticipated impact. This is a specification change announcement only. There are no changes

to the actual device.

Changes to product identification resulting from this PCN:

	None.							
Product Affected:								
	MSP430FR2000IPW16	MSP430FR2000IPW16R	MSP430FR2000IRLLR	MSP430FR2000IRLLT				
	MSP430FR2100IPW16	MSP430FR2100IPW16R	MSP430FR2100IRLLR	MSP430FR2100IRLLT				
	MSP430FR2110IPW16	MSP430FR2110IPW16R	MSP430FR2110IRLLR	MSP430FR2110IRLLT				
	MSP430FR2111IPW16	MSP430FR2111IPW16R	MSP430FR2111IRLLR	MSP430FR2111IRLLT				

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