

Vishay Siliconix

P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ)	
	0.045 at $V_{GS} = -4.5 \text{ V}$	- 9 ^a		
- 20	0.063 at $V_{GS} = -2.5 \text{ V}$	- 9 ^a	9 nC	
	0.088 at V _{GS} = - 1.8 V	- 9 ^a		

FEATURES

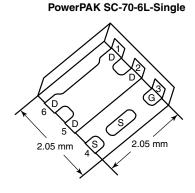
- · Halogen-free
- TrenchFET[®] Power MOSFET
- New Thermally Enhanced PowerPAK[®] SC-70 Package
 - Small Footprint Area
 - Low On-Resistance

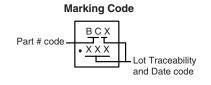


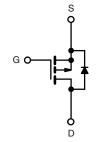
RoHS

APPLICATIONS

 Load Switch, PA Switch and Battery Switch for Portable Devices







Ordering Information: SiA443DJ-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise note	ed		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 20	V		
Gate-Source Voltage		V _{GS}	± 8	v	
Continuous Drain Current (T _{.1} = 150 °C)	$T_C = 25 ^{\circ}C$ $T_C = 70 ^{\circ}C$	I _D	- 9 ^a - 9 ^a		
	$T_A = 25 ^{\circ}\text{C}$ $T_A = 70 ^{\circ}\text{C}$		- 6.7 ^{b, c} - 5.4 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	- 20		
Continuous Source-Drain Diode Current	$T_C = 25 ^{\circ}C$ $T_A = 25 ^{\circ}C$	I _S	- 9 ^a - 2.7 ^{b, c}	_	
Maximum Power Dissipation	T _C = 25 °C T _C = 70 °C	P _D	15 9.8	w	
Maximum Fower Dissipation	$T_A = 25 ^{\circ}\text{C}$ $T_A = 70 ^{\circ}\text{C}$, n	3.3 ^{b, c} 2.1 ^{b, c}		
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	- °C		
Soldering Recommendations (Peak Temperature		260			

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, f}	t ≤ 5 s	R _{thJA}	30	38	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	6.5	8.1] C/W	

Notes:

- a. Package limited.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. See Solder Profile (http://www.vishay.com/ppg?73257). The PowerPAK SC-70 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under Steady State conditions is 80 °C/W.

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SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static				1	ı	_		
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V, } I_{D} = -250 \mu\text{A}$	- 20			V		
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$ $\Delta V_{GS(th)}/T_{J}$	I _D = - 250 μA		- 19.5		mV/°C		
V _{GS(th)} Temperature Coefficient				2.3				
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.4		- 1	V		
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V$, $V_{GS} = \pm 8 V$			± 100	ns		
Zero Gate Voltage Drain Current	1	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	μΑ		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			- 10			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 20			Α		
		$V_{GS} = -4.5 \text{ V}, I_D = -4.7 \text{ A}$		0.037	0.045	†		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -3.9 \text{ A}$		0.052	0.063	Ω		
		V _{GS} = - 1.8 V, I _D = - 1.1 A		0.072	0.088			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 4.7 A		14		S		
Dynamic ^b				1				
Input Capacitance	C _{iss}			750				
Output Capacitance	C _{oss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		140		pF		
Reverse Transfer Capacitance	C _{rss}	, d3 ,		100				
Tieveree Transier Capacitanies	rss	V _{DS} = - 10 V, V _{GS} = - 8 V, I _D = - 6.8 A		15	25	nC		
Total Gate Charge	Q _g Q _{gs} Q _{qd}	V _{DS} = -10 V, V _{GS} = -4.5 V, I _D = -6.8 A		9	14			
Gate-Source Charge				1.4				
Gate-Drain Charge		23 - 7 do - 7 D		2.7				
Gate Resistance	R _g	f = 1 MHz		9		Ω		
Turn-On Delay Time	t _{d(on)}	· · · · · · · · · · · · · · · · · · ·		16	25			
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_{L} = 1.9 \Omega$		100	150	- - -		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong -5.4 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		40	60			
Fall Time	t _f	, GEN - , y		70	105			
Turn-On Delay Time	· .			5	103	ns		
Rise Time	t _{d(on)}	$V_{DD} = -10 \text{ V}, R_1 = 1.9 \Omega$		15	25	-		
Turn-Off Delay Time				35	55			
Fall Time	t _{d(off)}			75	110			
Drain-Source Body Diode Characterist	<u> </u>			/3	110			
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 9			
Pulse Diode Forward Current	I _{SM}	.0 20 0	 		20	Α		
Body Diode Voltage	V _{SD}	I _S = - 5.4 A, V _{GS} = 0 V		- 0.8	- 1.2	V		
Body Diode Reverse Recovery Time		15 - 5 , * G5 - 5 *		25	50			
<u> </u>	t _{rr}					ns		
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = - 5.4 A, di/dt = 100 A/μs, T _J = 25 °C		12	24	nC		
Reverse Recovery Fall Time	t _a			9		ns		
Reverse Recovery Rise Time	t _b			16				

Notes:

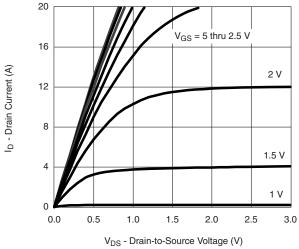
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

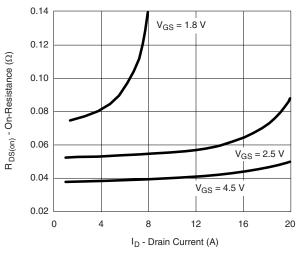


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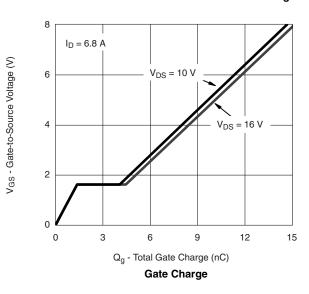
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

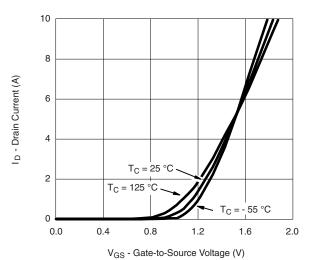


Output Characteristics

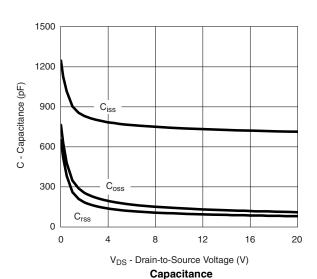


On-Resistance vs. Drain Current and Gate Voltage





Transfer Characteristics



 $I_D = 4.7 A$ V_{GS} = 4.5 V, 2.5 V, 1.8 V 1.4 R_{DS(on)} - On-Resistance (Normalized) 1.2 1.0 0.8 0.6 - 25 0 25 100 125 150 - 50 50 75 T_J - Junction Temperature (°C)

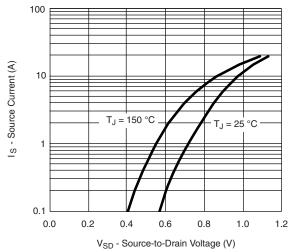
On-Resistance vs. Junction Temperature

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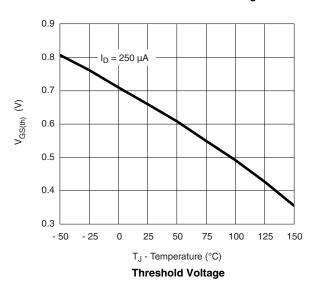
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

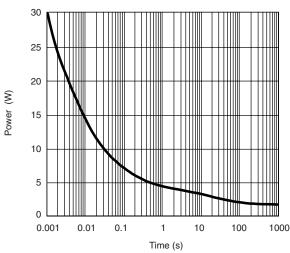


Soure-Drain Diode Forward Voltage

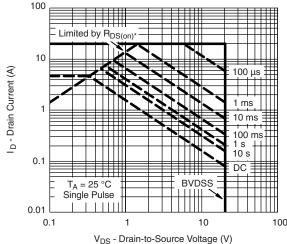


0.12 0.10 0.00

On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



 * V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

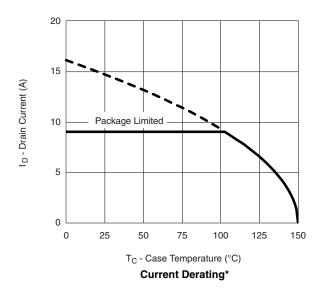
Safe Operating Area, Junction-to-Ambient

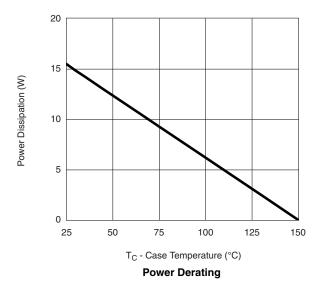




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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





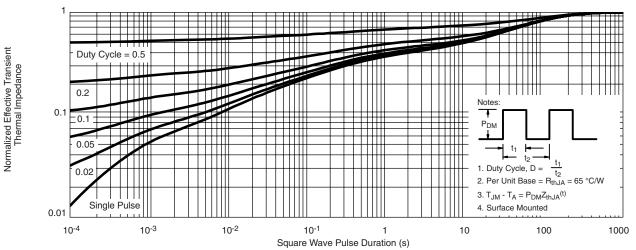
^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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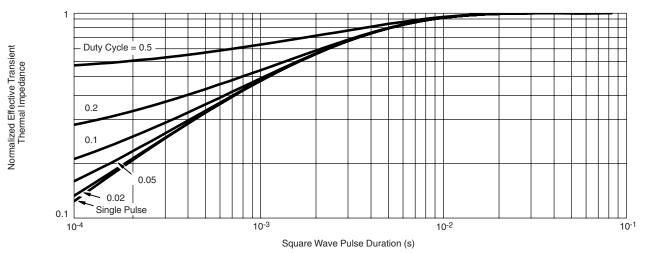
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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