



Product/Process Change Notice - PCN 17_0057 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

PCN Title: DDR Timing Specification Correction in the ADV7181D Datasheet
Publication Date: 29-May-2017
Effectivity Date: 29-May-2017 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:

Initial Release

Description Of Change

ADV7181D datasheet Data Output Transition Time DDR specification correction.

Current datasheet (Rev. A) specifies the following DDR (CP) Data Output Transition Time:

Positive clock edge to end of valid data: $t_{15} \text{ min} = -4 + T_{LLC}/4 \text{ ns}$
Positive clock edge to start of valid data: $t_{16} \text{ min} = 0.25 + T_{LLC}/4 \text{ ns}$
Negative clock edge to end of valid data: $t_{17} \text{ min} = -2.95 + T_{LLC}/4 \text{ ns}$
Negative clock edge to start of valid data: $t_{18} \text{ min} = -0.5 + T_{LLC}/4 \text{ ns}$

Next revision datasheet (Rev. B) will specify the following DDR (CP) Data Output Transition Time:

Positive clock edge to end of valid data: $t_{15} \text{ min} = 1.9 \text{ ns}$
Start of valid data to positive clock edge: $t_{16} \text{ min} = 1.7 \text{ ns}$
Negative clock edge to end of valid data: $t_{17} \text{ min} = 1.4 \text{ ns}$
Start of valid data to negative clock edge: $t_{18} \text{ min} = 1.7 \text{ ns}$

Reason For Change

Update datasheet to accurately reflect correct timing specification.

Impact of the change (positive or negative) on fit, form, function & reliability

There has been no change to the device silicon, there will be no impact on the device performance.

Summary of Supporting Information

The ADV7181D Datasheet Rev B will reflect the correct timing specifications.

Supporting Documents

Attachment 1: Type: Datasheet Specification Comparison

ADI_PCN_17_0057_Rev_-_ADV7181D_Timing_Specification_Update.pdf

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas: PCN_Americas@analog.com

Europe: PCN_Europe@analog.com

Japan: PCN_Japan@analog.com

Rest of Asia: PCN_ROA@analog.com

Appendix A - Affected ADI Models

Added Parts On This Revision - Product Family / Model Number (4)

ADV7181D / ADV7181DBCZ

ADV7181D / ADV7181DBCZ-RL

ADV7181D / ADV7181DWBCZ

ADV7181D / ADV7181DWBCZ-RL

Appendix B - Revision History

Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	29-May-2017	29-May-2017	Initial Release

Analog Devices, Inc.

DocId:4058 Parent DocId:None Layout Rev:7