RoHS

COMPLIANT

HALOGEN

FREE

Vishay Semiconductors

High Performance Schottky Rectifier, 1.5 A



- Extremely low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Surface mountable
- Compact size
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Switching power supplies
- Meter protection
- · Reverse protection for power input to PC board circuits
- Battery isolation and charging
- Low threshold voltage diode
- Freewheeling or by-pass diode
- · Low voltage clamp

DESCRIPTION

The VS-15MQ040-M3 Schottky rectifier is designed to be used for low power applications where a reverse voltage of 40 V is encountered and surface mountable is required.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	1.5	А		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 μs sine	330	А		
V _F	2 A _{pk} , T _J = 125 °C	0.43	V		
TJ	Range	-40 to +150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-15MQ040-M3	UNITS	
Maximum DC reverse voltage	V _R	40	V	
Maximum working peak reverse voltage	V _{RWM}	49	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at $T_L = 105$ °C, rectangular waveform On PC board 9 mm ² island (0.013 mm thick copper pad area)		2.1	Α
See fig. 4		50 % duty cycle at T_L = 113 °C, rectangular waveform On PC board 9 mm ² island (0.013 mm thick copper pad area)		1.5	
Maximum peak one cycle	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated	330	_
non-repetitive surge current See fig. 6		10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	140	A
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А

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SMA (DO-214AC)

I_{F(AV)}

V_F at I_F

T_J max.

Package

Circuit configuration

 V_R

I_{RM}

E_{AS}

PRIMARY CHARACTERISTICS



1.5 A

40 V

0.34 V

20 mA at 125 °C

6.0 mJ

150 °C

SMA (DO-214AC)

Single



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	1.5 A	T ₁ = 25 °C	0.43	V
		2 A	1j=25 0	0.49	
		1.5 A	T.I = 125 °C	0.34	
		2 A		0.43	
Maximum reverse leakage current See fig. 2	I _{RM}	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.5	mA
		T _J = 125 °C	v _R = naleu v _R	20	
Threshold voltage	V _{F(TO)}	$T_{\rm J} = T_{\rm J}$ maximum		0.26	V
Forward slope resistance	r _t			64.6	mΩ
Typical junction capacitance	C _T	$V_R = 10 V_{DC}$, $T_J = 25 $ °C, test signal = 1 MHz		134	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

⁽¹⁾ Pulse width = 300 μ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
Approximate weight			0.07	g
			0.002	oz.
Marking device		Case style SMA (DO-214AC)	Х	F

Note

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ (1)



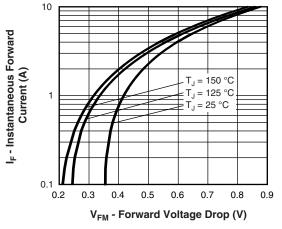


Fig. 1 - Maximum Forward Voltage Drop Characteristics

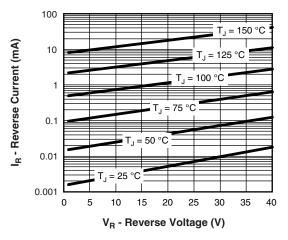


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

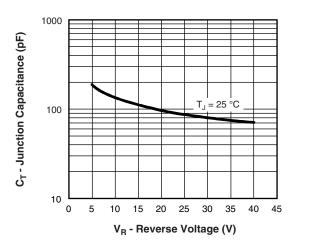


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \\ \end{array}$

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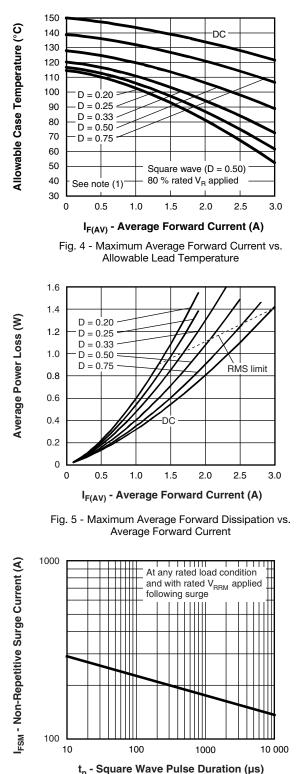


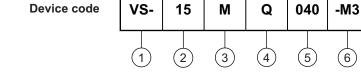
Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

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SHA

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ORDERING INFORMATION TABLE

- Vishay Semiconductors product 1
 - Current rating _ M = SMA _
- 2 3 4 5 Q = Schottky "Q" series -
 - Voltage rating (040 = 40 V) -
- 6 Environmental digit: -

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-15MQ040-M3/5AT	5AT	7500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95400	
Part marking information	www.vishay.com/doc?95403	
Packaging information	www.vishay.com/doc?95404	



Vishay Semiconductors



Outline Dimensions

Vishay Semiconductors

SMA

DIMENSIONS in inches (millimeters)

DO-214AC (SMA)





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